



# SERVICE MANUAL

VHF FM TRANSCEIVERS

**IC-F3021T**  
**IC-F3022T**  
**IC-F3023T**  
**IC-F3021S**  
**IC-F3022S**  
**IC-F3023S**

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S-14303HZ-C1  
July 2006

Icom Inc.

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## INTRODUCTION

This service manual describes the latest service information for the **IC-F3021T/S**, **IC-F3022T/S** and **IC-F3023T/S** VHF FM TRANSCEIVERS at the time of publication.

MODEL	Key Pad	VERSION	SYMBOL	Channel Spacing
IC-F3021T	10-key	U.S.A	[USA-02]	15.0 kHz / 30.0 kHz
IC-F3021S	4-key			
IC-F3022T	10-key	Europe	[EUR-02]	12.5 kHz / 20.0 kHz / 25.0 kHz
IC-F3022S	4-key			
IC-F3023T	10-key	General	[GEN-02]	12.5 kHz / 25.0 kHz
IC-F3023S	4-key			

**To upgrade quality, any electrical or mechanical parts and internal circuits are subject to change without notice or obligation.**

## CAUTION

**NEVER** connect the transceiver to an AC outlet or to a DC power supply that uses more than 8 V. This will ruin the transceiver.

**DO NOT** expose the transceiver to rain, snow or any liquids.

**DO NOT** reverse the polarities of the power supply when connecting the transceiver.

**DO NOT** apply an RF signal of more than 20 dBm (100 mW) to the antenna connector. This could damage the transceiver's front end.

## ORDERING PARTS

Be sure to include the following four points when ordering replacement parts:

1. 10-digit Icom parts numbers
2. Component name and informations
3. Equipment model name and unit name
4. Quantity required

### <SAMPLE ORDER>

5030002760 LCD L3-0200HAY-3 IC-F3021T Main unit 5 pieces  
8810009561 Screw PH BT M2 x 6 NI-ZK3 IC-F3021T Chassis 10 pieces

Addresses are provided on the inside back cover for your convenience.



## REPAIR NOTES

1. Make sure a problem is internal before disassembling the transceiver.
2. **DO NOT** open the transceiver until the transceiver is disconnected from its power source.
3. **DO NOT** force any of the variable components. Turn them slowly and smoothly.
4. **DO NOT** short any circuits or electronic parts. An insulated turning tool **MUST** be used for all adjustments.
5. **DO NOT** keep power ON for a long time when the transceiver is defective.
6. **DO NOT** transmit power into a signal generator or a sweep generator.
7. **ALWAYS** connect a 40 dB to 50 dB attenuator between the transceiver and a deviation meter or spectrum analyzer when using such test equipment.
8. **READ** the instructions of test equipment thoroughly before connecting equipment to the transceiver.

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# SECTION 1 SPECIFICATIONS

			[USA], [GEN]	[EUR]	
GENERAL	• Frequency coverage		136–174 MHz		
	• Type of emission	Wide	16K0F3E (25.0 kHz)		
		Middle	–	14K0F3E (20.0 kHz)	
		Narrow	11K0F3E (12.5 kHz)	8K50F3E (12.5 kHz)	
	• Number of programable channels		128 channels (8 zones)		
	• Antenna impedance		50 Ω (nominal)		
	• Operating temperature range		–22°F to +140°F	–25°C to +55°C	
	• Power supply requirement		Specified Icom's battery packs only (Operatable voltage; 7.2 V DC negative ground)		
	• Current drain (approx.)	RX	Stand-by	75 mA	
			Max.audio	250 mA	
TX		at 5 W	1.5 A		
		at 1 W	0.7 A		
• Dimensions (projections not included)		2 3/32 (W) × 4 23/32 (H) × 1 9/32 (D) in	53.0 (W) × 120.0 (H) × 32.5 (D) mm		
• Weight (with BP-231, approx.)		9.2 oz	260 g		
TRANSMITTER	• Transmit output power		5 W (High), 2 W (Low2), 1 W (Low1)		
	• Modulation		Variable reactance frequency modulation		
	• Max. permissible deviation	Wide	±5.0 kHz		
		Middle	–	±4.0 kHz	
		Narrow	±2.5 kHz		
	• Frequency error		±2.5 ppm	±1.5 kHz	
	• Spurious emission		80 dB typ.	0.25 μW (≤1 GHz), 1.0 μW (>1 GHz)	
	• Adjacent channel power	Wide	More than 70 dB (80 dB typ.)		
		Middle	–	More than 70 dB (80 dB typ.)	
		Narrow	More than 60 dB (70 dB typ.)		
	• Audio harmonic distortion		3% typ. (with 1 kHz AF 40% deviation)		
	• FM hum and noise (without CCITT filter)	Wide	More than 40 dB (46 dB typ.)	–	
		Narrow	More than 34 dB (40 dB typ.)	–	
	• Residual modulation (with CCITT filter)	Wide	–	More than 45 dB (53 dB typ.)	
Middle		–	More than 43 dB (51 dB typ.)		
Narrow		–	More than 40 dB (48 dB typ.)		
• Limiting charact of modulation		60–100% of max. deviation			
• Microphone impedance		2.2 kΩ			
RECEIVER	• Receive system		Double-conversion superheterodyne		
	• Intermediate frequencies		1st IF; 46.35 MHz, 2nd IF; 450 kHz		
	• Sensitivity		0.25 μV typ. at 12 dB SINAD	–4 dBμV (EMF) typ. at 20 dB SINAD	
	• Squelch sensitivity (at threshold)		0.25 μV typ.	–4 dBμV (EMF) typ.	
	• Adjacent channel selectivity	Wide	More than 70 dB (75 dB typ.)		
		Middle	–	More than 70 dB (75 dB typ.)	
		Narrow	More than 60 dB (65 dB typ.)		
	• Spurious response		More than 70 dB		
	• Intermodulation		More than 70 dB (75 dB typ.)	More than 65 dB (67 dB typ.)	
	• Audio output power		0.5 W typ. at 5% distortion with an 8 Ω load		
• Audio output impedance		8 Ω			

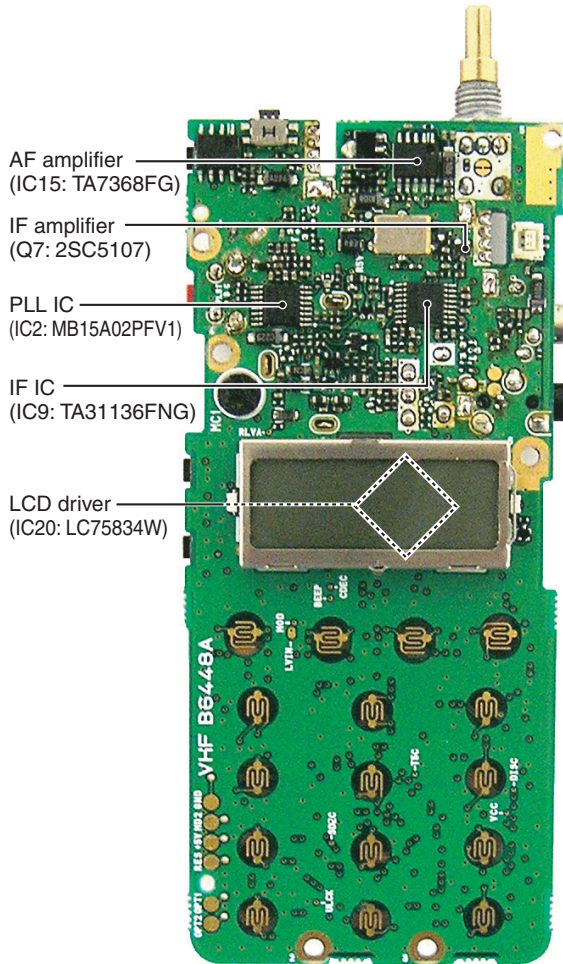
Measurements made in accordance with EIA-152-C/204D, TIA-603 ([USA], [GEN]) or EN 300 086 ([EUR]).

All stated specifications are subject to change without notice or obligation.

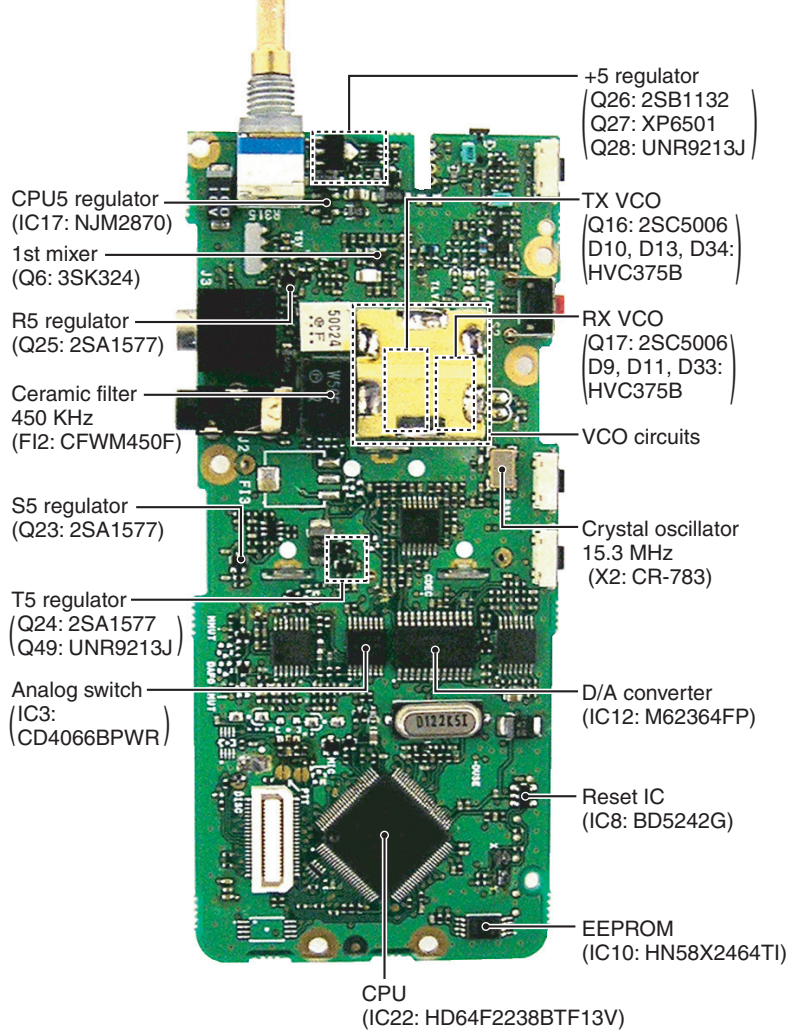
# SECTION 2 INSIDE VIEWS

## MAIN UNIT

TOP VIEW

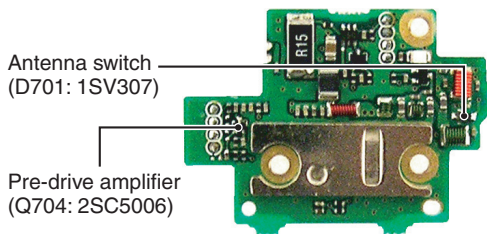


BOTTOM VIEW

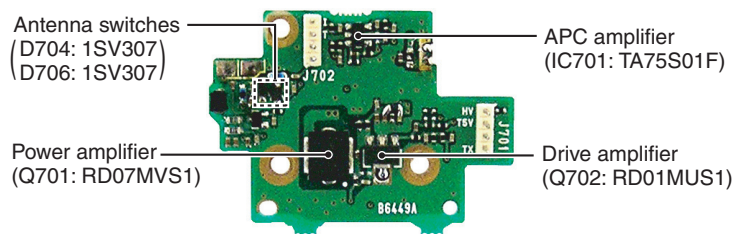


## PA UNIT

TOP VIEW



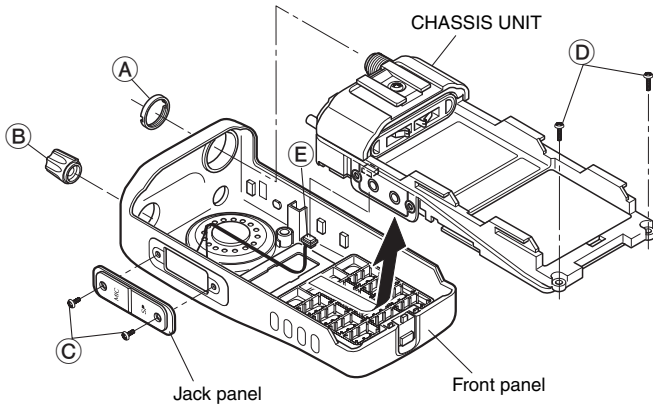
BOTTOM VIEW



# SECTION 3 DISASSEMBLY INSTRUCTIONS

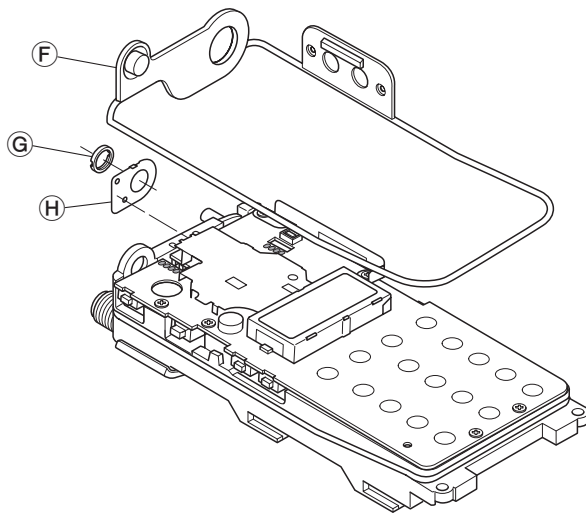
## 1 REMOVING THE CHASSIS UNIT

- ① Unscrew ANT nut (A), and remove knob (B).
- ② Unscrew 2 screws (C), and remove the jack panel.
- ③ Unscrew 2 screws (D), and unplug the connector (E) from the chassis unit.
- ④ Take off the chassis unit in the direction of the arrow from the front panel.



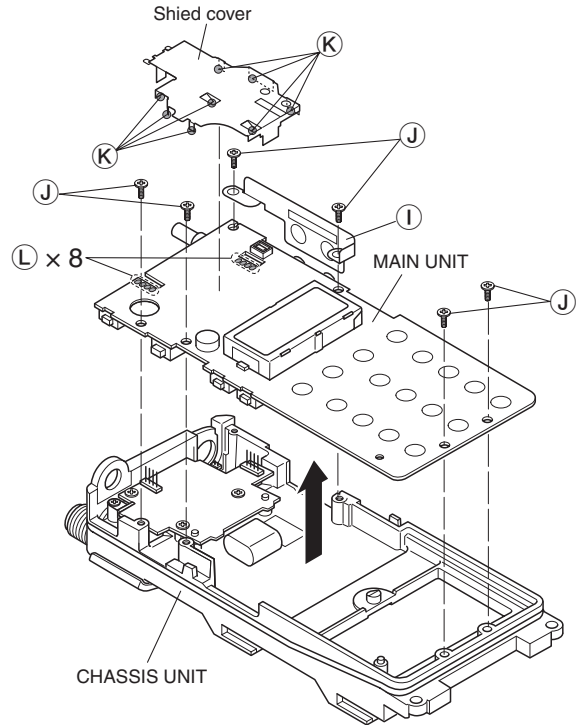
## 2 REMOVING THE MAIN UNIT

- ① Remove the main seal (F).
- ② Unscrew VR nut (G), and remove the top plate (H).



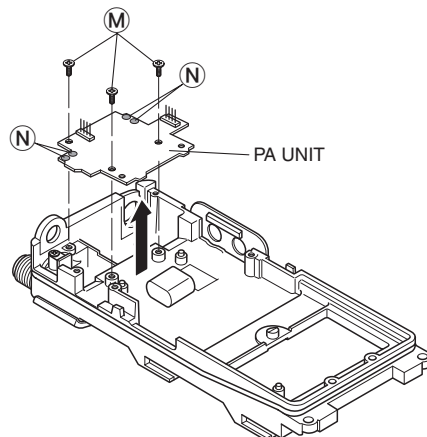
(Continued to right above)

- ③ Unscrew 6 screws (J).
- ④ Remove the side plate (I).
- ⑤ Unsolder 8 points (K), and remove the shield cover.
- ⑥ Unsolder 8 points (L), and take off the MAIN UNIT in the direction of the arrow.



## 3 REMOVING THE PA UNIT

- ① Unscrew 3 screws (M).
- ② Unsolder 4 points (N), and take off the PA UNIT in the direction of the arrow.

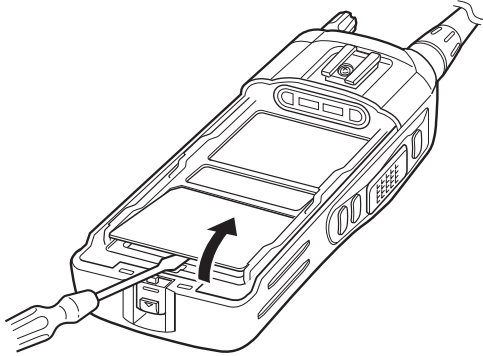


# SECTION 4 OPTIONAL UNIT INSTALLATION

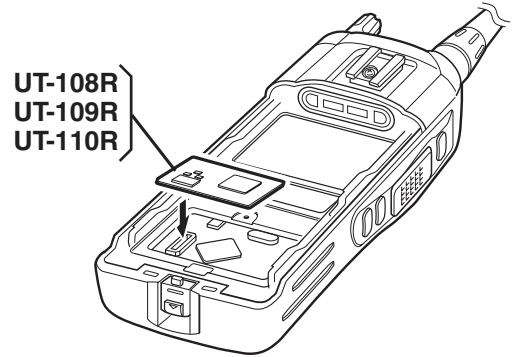
**CAUTION!** Optional unit installation should be done at authorized Icom service center only.

Install the optional unit as follows.

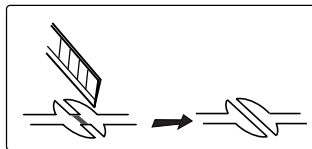
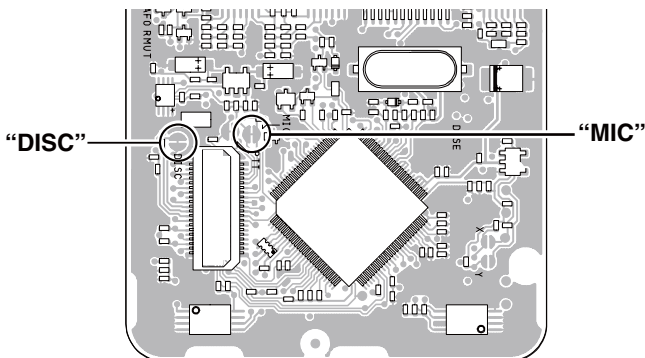
- ① Rotate [VOL] to turn the power OFF, and remove the battery pack.
- ② Remove the unit cover as below. (The removed unit cover can not be used again.)



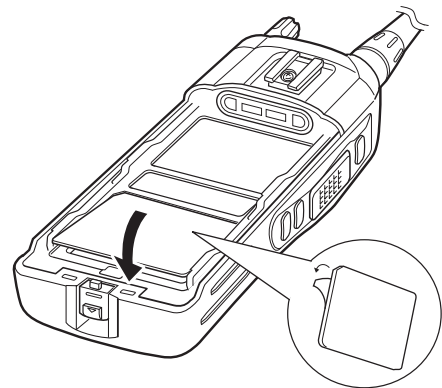
- ④ Install the unit as below.



- ③ Cut the pattern on the PC board at "MIC" and "DISC" as below. (This modification is not necessary for UT-108R installation.)



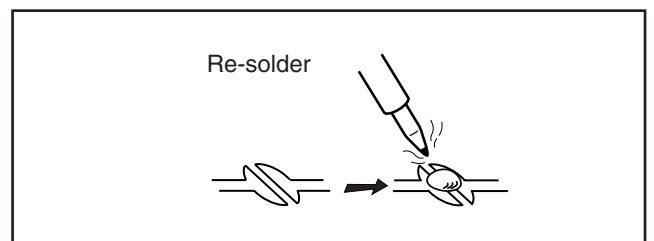
- ⑤ Remove the paper backing of the supplied unit cover, and attach the unit cover and the battery pack, then rotate [VOL] to turn the power ON.



- ⑥ Set or modify the scrambler or decoder settings using optional CS-F3020.

**NOTE: When uninstalling the unit**

Be sure to re-solder the cut points as below when you remove the unit. Otherwise, no transmit modulation or receive AF output is available.



# SECTION 5 CIRCUIT DESCRIPTION

## 5-1 RECEIVER CIRCUITS

### 5-1-1 ANTENNA SWITCHING CIRCUIT (PA UNIT)

The antenna switching circuit toggles the receive (RX) line and transmit (TX) line.

The received signals from the antenna are passed through the low-pass filter (ANT UNIT; L801, L802, C802, C803, C807) and antenna switch (D701, D704, D706).

While transmitting, the voltage on the T5V line is applied to D701, D704 and D706, and these are ON. Thus the TX line is connected to the antenna. Simultaneously, the RX line is connected to the ground (GND) to prevent transmit signal entering.

While receiving, no voltage is applied to the D701, D704 and D706, and these are OFF. Thus the TX line and the antenna are disconnected to prevent received signals entering. Simultaneously, the RX line is disconnected from the GND and the received signals are passed through the low-pass filter (L712, L714, C750, C751). The filtered signals are applied to the RF circuits.

### 5-1-2 RF CIRCUITS (MAIN UNIT)

RF circuits filter and amplify the received signals within the frequency coverage.

The received signals from the antenna switching circuit are passed through the two-staged bandpass filter (BPF; D19, D24, L7, L8, C22, C25, C27–C29, C369) to filter-out unwanted signals, and the filtered signals are applied to the RF amplifier (Q5). The amplified received signals are then applied to the 1st mixer (Q6) via another BPF (L47–L49, C19, C40, C437–C439, C443).

### 5-1-3 1st IF CIRCUITS (MAIN UNIT)

The received signals are converted into the 1st IF signal, filtered and amplified at the 1st IF circuits.

The received signals from the BPF (L47–L49, C19, C40, C437–C439, C443) are applied to the 1st mixer (Q6) and converted into the 46.35 MHz 1st IF signal by being mixed with the local oscillator (LO) signal from the RX VCO (Q17, D9, D11, D33).

The converted 1st IF signal is passed through the 1st IF filter (FI1) to filter-out adjacent signals, then applied to the 1st IF amplifier (Q7). The amplified 1st IF signal is then applied to the FM IF IC (IC9, pin 16).

### 5-1-4 2nd IF AND DEMODULATOR CIRCUITS (MAIN UNIT)

The 1st IF signal is converted into the 2nd IF signal, and demodulated.

The 1st IF signal from the 1st IF amplifier is applied to the 2nd mixer in the FM IF IC (IC9, pin 16), and converted into the 45.9 MHz 2nd IF signal by being mixed with the 45.9 MHz 2nd LO signal from the reference frequency oscillator (X2) via the tripler (Q22) and BPF (L33, C163, C164, C166). The converted 2nd IF signal is output from pin 3, and passed through the 2nd IF filters to remove sideband noise.

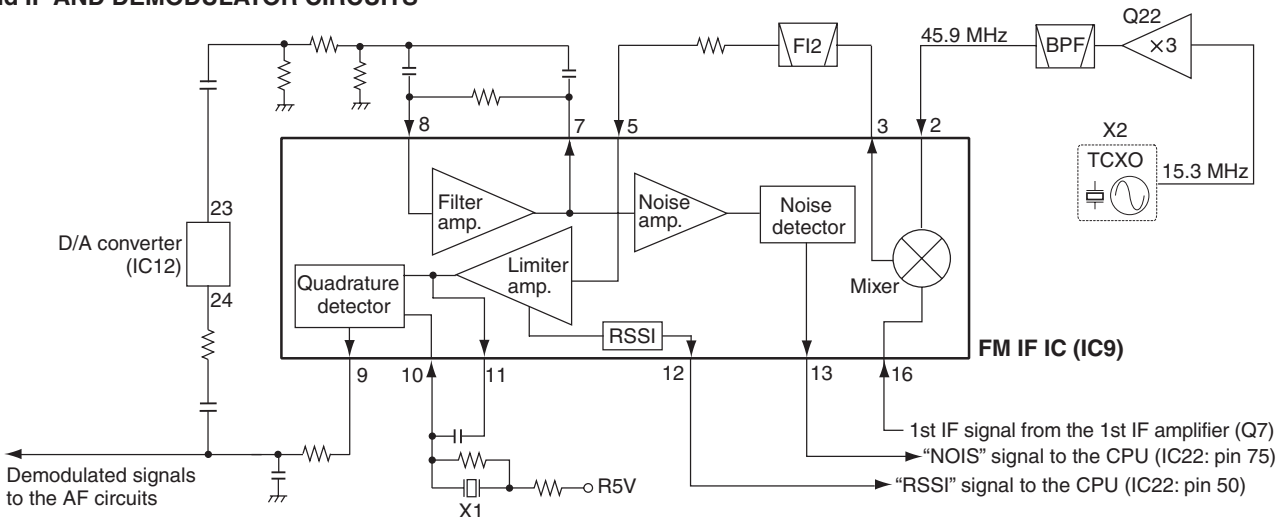
The 2nd IF signal is passed through the 2nd IF filter (FI2) and applied to the FM IF IC (IC9, pin 5) again. The filtered 2nd IF signal is amplified at the limiter amplifier, and FM-demodulated by the quadrature detector (IC9, pins 10, 11, X1). The demodulated AF signals are output from pin 9, then applied to the AF amplifier circuits.

### 5-1-5 AF AMPLIFIER CIRCUITS (MAIN UNIT)

The demodulated AF signals from the FM IF IC are amplified and filtered at AF circuits.

The demodulated AF signals from the FM IF IC (IC9, pin 9) are passed through high-pass filter (HPF; IC5, pins 2, 1) to remove tone signals. The filtered AF signals are passed through the de-emphasis circuit (R142, C249) to obtain the 6 dB/oct of frequency characteristic. The de-emphasized AF signals are passed through the RX mute switch (Q32, Q33), AF switch (Q36, Q37), HPF (IC5, pins 13, 14), analog switch (IC3, pins 1, 2), AF mixer (IC5, pins 6, 7) and analog switch (IC3, pins 10, 11) in sequence.

## • 2nd IF AND DEMODULATOR CIRCUITS





The AF signals from the analog switch (IC3, pin 11) are applied to the volume buffer amplifier (IC6, pin 9). The buffer-amplified AF signals are adjusted its level (= audio level) by volume control pot (R315), then applied to the AF power amplifier (IC15, pin 4) and amplified to the 0.5 W of audio output power (max., at 8  $\Omega$  load).

The power-amplified AF signals are output from pin 10, then applied to the internal speaker (CHASSIS; SP1) or an external speaker via [SP] jack (J2).

## 5-1-6 SQUELCH CIRCUITS (MAIN UNIT)

### 5-1-6-1 NOISE SQUELCH

The squelch mutes the AF output signals when no RF signal is received. By detecting noise components (around 30 kHz signals) in the demodulated AF signals, the squelch circuit toggles the mute switch and AF power amplifier ON and OFF.

A portion of the demodulated AF signals from the FM IF IC (IC9, pin 9) is applied to the D/A converter (IC12, pin 24) for level (= squelch threshold) adjustment. The level-adjusted AF signals are output from pin 23 and passed through the noise filter (IC9, pins 8, 7, R42, R44–R46, C69, C70, C413). The filtered noise signals are amplified the noise components only at the noise amplifier.

The amplified noise components are converted into the pulse-type signal at the noise detector section, and output from pin 13 as the “NOIS” signal. The converted signal is applied to the CPU (IC22, pin 75). Then the “RMUTE” signal from the CPU (IC22, pin 96) to the RX mute switch (Q32) and analog switch (IC3, pins 12, 13) becomes “Low” according to the “NOIS” signal level to cut off the AF line.

At the same time, the “AFON” signal from the CPU (IC22, pin 70) to the AF amplifier controller (Q41, Q42, D21, D23) becomes “Low” and the controller turns the AF power amplifier (IC15) OFF.

### 5-1-6-2 TONE SQUELCH

#### • CTCSS/DTCS

The tone squelch circuit detects tone signals and opens the squelch only when receiving a signal containing a matched sub audible tone. When the tone squelch is in use, and a signal with a mismatched or no sub audible tone is received, the tone squelch circuit mutes the AF signals even when the noise squelch is open.

A portion of the demodulated AF signals is passed through the active LPF (Q39) to filters CTCSS/DTCS signal. The filtered signal is applied to the CPU (IC22, pin 46). The CPU compares the applied signal and the set CTCSS/DTCS, then the CPU controls the status (“Low” or “High”) of “RMUTE” and “AFON” signals as same as “NOISE SQUELCH”.

#### • DTMF

DTMF signals in the demodulated AF signals are passed through the LPF (IC6, pins 5, 7) to remove unwanted components (voice signals), then applied to the CPU (IC22, pin 45) and decoded.

## 5-2 TRANSMITTER CIRCUITS

### 5-2-1 MICROPHONE AMPLIFIER CIRCUITS (MAIN UNIT)

The AF signals from the microphone (MIC signals) are filtered and level-adjusted at microphone amplifier circuits.

#### • MIC SIGNALS

MIC signals from the microphone are applied to or bypassed the ALC (Automatic Level Control) circuit (IC24, pins 3, 5) and the A/D switch (IC25, pins 7, 1), then applied to the D/A converter (IC12, pin 1).

The level-adjusted MIC signals are output from pin 2, and passed through the MIC mute switch (Q31), HPF (IC5, pins 13, 14) and gain switch (Q34) which controls the gain of MIC amplifier (IC5) according to the Analog/Digital mode, then applied to the MIC amplifier (IC5, pin 9). The amplified MIC signals are output from pin 8, and passed through the analog switch (IC3, pins 4, 3), AF mixer (IC5, pins 6, 7) where the MIC signals and tone signals are mixed with.

#### • TONE SIGNALS

The CTCSS/DTCS signals are generated by the CPU (IC22) and output from pins 19–21. The output signals are passed through the 3 resistors (R222–R224) to change its waveform. The waveform changed CTCSS/DTCS signals are passed through the LPF (IC7, pins 10, 8), tone filter switch (Q40) and D/A converter (IC12, pins 12, 11) for level adjustment. The level adjusted CTCSS/DTCS signals are then applied to the AF mixer (IC5, pin 6).

DTMF signals are generated by the CPU (IC22) and output from pin 43. The output DTMF signals are passed through two LPFs (IC6, pins 3, 1 and pins 12, 14), then applied to the AF mixer (IC5, pin 6).

The mixed AF signals are output from pin 7 of the AF mixer (IC5) and passed through the analog switch (IC3, pins 9, 8), then applied to the AF amplifier (IC7, pin 6). The amplified AF signals are output from pin 7, and applied to the D/A converter (IC12, pin 9) to be adjusted its level (= deviation). The level-adjusted MIC signals are then applied to the modulation circuits as the modulation signals.

### 5-2-2 MODULATION CIRCUITS (MAIN UNIT)

The modulation circuits modulate the VCO oscillating signal using the modulation signals.

The modulation signals from the D/A converter (IC12, pin 10) are applied to the D12 at the TX VCO (Q16, D10, D13, D34) to modulate the VCO oscillating signal by changing the reactance of D12.

The modulation signals are also applied to the reference frequency oscillator (X2) via D/A converter (IC12, pins 16, 15) and the buffer (IC7, pins 12, 14), to ensure the modulation of lower frequency components of the modulation signals.

The modulated VCO output is buffer-amplified by Q15 and Q29, then applied to the transmit amplifiers as the transmit signal via TX/RX switches (D16 is ON, D17 is OFF).

### 5-2-3 TRANSMIT AMPLIFIERS (PA UNIT)

The transmit signal from the VCO is amplified to the transmit output level by the transmit amplifiers.

The transmit signal from the TX/RX switches (MAIN UNIT; D16 is ON, D17 is OFF) is amplified by the pre-drive amplifier (Q704), drive amplifier (Q702) and power amplifier (Q701) in sequence to obtain 5 W (approx.) of transmit output power.

The power-amplified transmit signal is passed through the antenna switch (D701), then applied to the antenna via the LPF (ANT UNIT; L801, L802, C802, C803, C807).

### 5-2-4 APC CIRCUIT (PA UNIT)

The APC (Automatic Power Control) circuit prevents the transition of the transmit output power level which is caused by load mismatching or heat effect, etc.

A portion of transmit signal is detected by the transmit power detectors (D702, D703) to produce DC voltage corresponding to the transmit output power level. The detected voltage is applied to the APC amplifier (IC701, pin 3). The transmit power setting voltage "APC (T1)" from the D/A converter (MAIN UNIT; IC23, pin 1) is applied to another input terminal (pin 1) as the reference voltage.

The APC amplifier compares the detected voltage and reference voltage, and the difference of the voltage "APCV" is output from pin 4. The voltage "APCV" controls the bias of the pre-drive (Q704), drive (Q702) and power (Q701) amplifiers to reduce/increase the gain of these amplifiers for stable transmit output power.

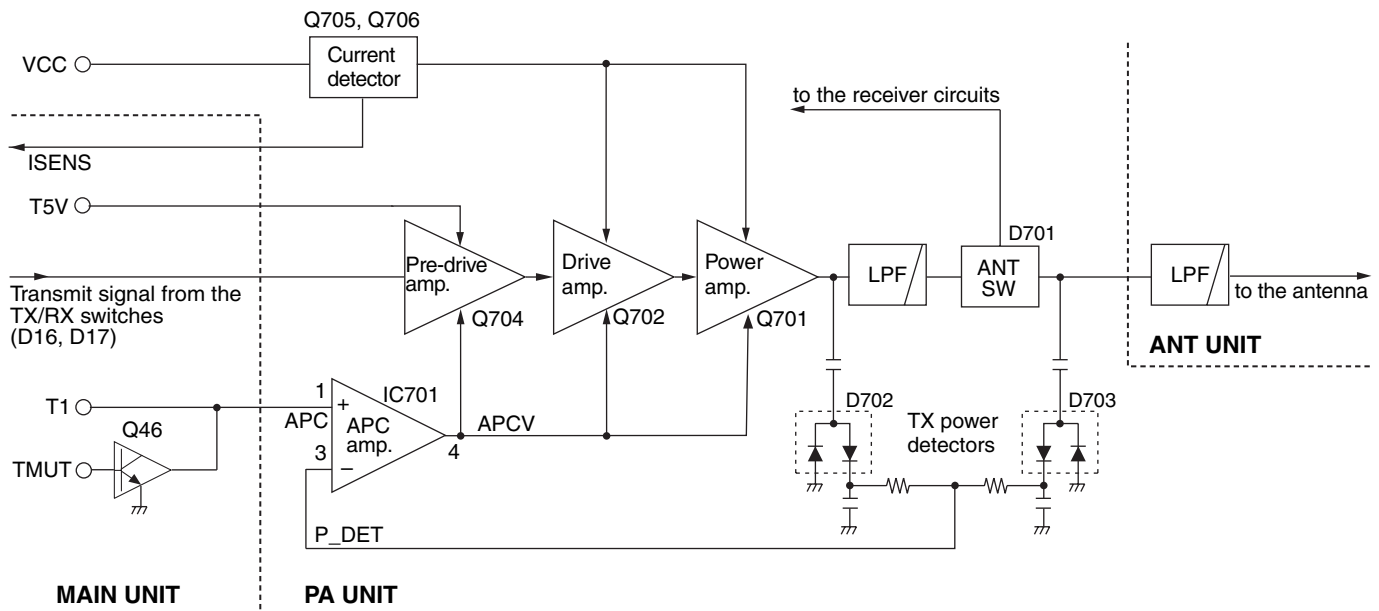
The transmit power muting is carried out by the TX mute switch (MAIN UNIT; Q46), using the "TMUT" signal from the CPU (MAIN UNIT; IC22, pin 35).

### 5-2-5 OVER CURRENT DETECTION CIRCUIT (PA UNIT)

The driving current of the drive (Q702) and power (Q701) amplifiers is detected at the current detector (Q705, Q706) by detecting the difference of voltage between both terminals of R714. The detected voltage "ISENS" is applied to the CPU (MAIN UNIT; IC22, pin 47).

In case of the over current is detected, the CPU outputs "TMUT" signal from pin 35 to TX mute switch (MAIN UNIT; Q46) to stop the transmitting to protect the transmit amplifiers (Q701, Q702, Q704).

#### • APC CIRCUIT



## 5-3 PLL CIRCUITS

### 5-3-1 VOLTAGE CONTROLLED OSCILLATORS (VCOs; MAIN UNIT)

VCO is an oscillator whose oscillating frequency is controlled by adding voltage (lock voltage).

This transceiver has 2 VCOs RX VCO (Q17, D9, D11, D33) and TX VCO (Q16, D10, D13, D34). The RX VCO oscillates the 1st LO signals, and the TX VCO oscillates the transmit signal.

#### • RX VCO

The output signals are amplified by the buffer amplifiers (Q15, Q29), and applied to the 1st mixer (Q6) via TX/RX switches (D16 is OFF, D17 is ON) and LPF (L46, C396, C397), to be mixed with the received signals to produce the 46.35 MHz 1st IF signal.

#### • TX VCO

The output signal is applied to the transmit amplifiers via the buffer amplifiers (Q15, Q29) and TX/RX switches (D16 is ON, D17 is OFF).

A portion of each VCO output is applied to the PLL IC (IC2, pin 8) via the buffer amplifier (Q15), doubler (Q14), BPF (D31, D32, L32, C196, C197, C199, C200, C205) and LPF (L34, L36, C202–C204).

### 5-3-2 PLL IC (MAIN UNIT)

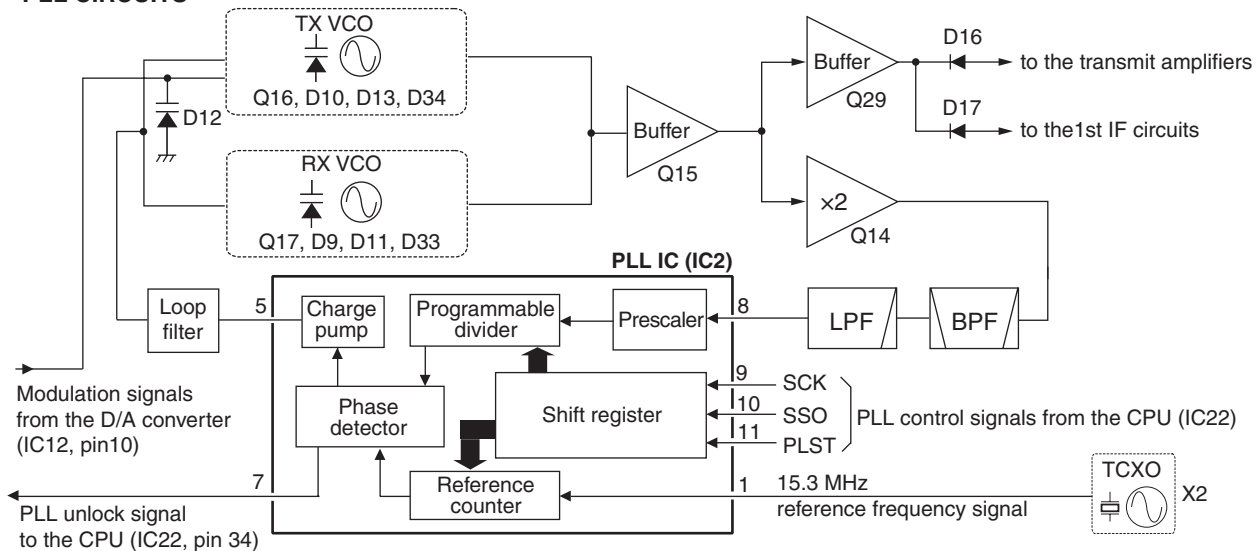
The PLL circuit provides stable oscillation of the transmit frequency and receive 1st LO frequency. The PLL output frequency is controlled by the divided ratio (N-data) from the CPU.

The VCO output signal from the LPF (L34, L36, C202–C204) is applied to the PLL IC (IC2, pin 8). The applied signal is divided at the prescaler and programmable counter according to the "SSO" signal from the CPU (IC22, pin 99). The divided signal is phase-compared with the reference frequency signal from the reference frequency oscillator (X2), at the phase detector.

The phase difference is output from pin 5 as a pulse type signal after being passed through the internal charge pump. The output signal is converted into the DC voltage (lock voltage) by passing through the loop filter (R94–R96, C16, C17, C146). The lock voltage is applied to the varactors (D9 and D33 of RX VCO, D10 and D34 of TX VCO) and locked to keep the VCO frequency constant.

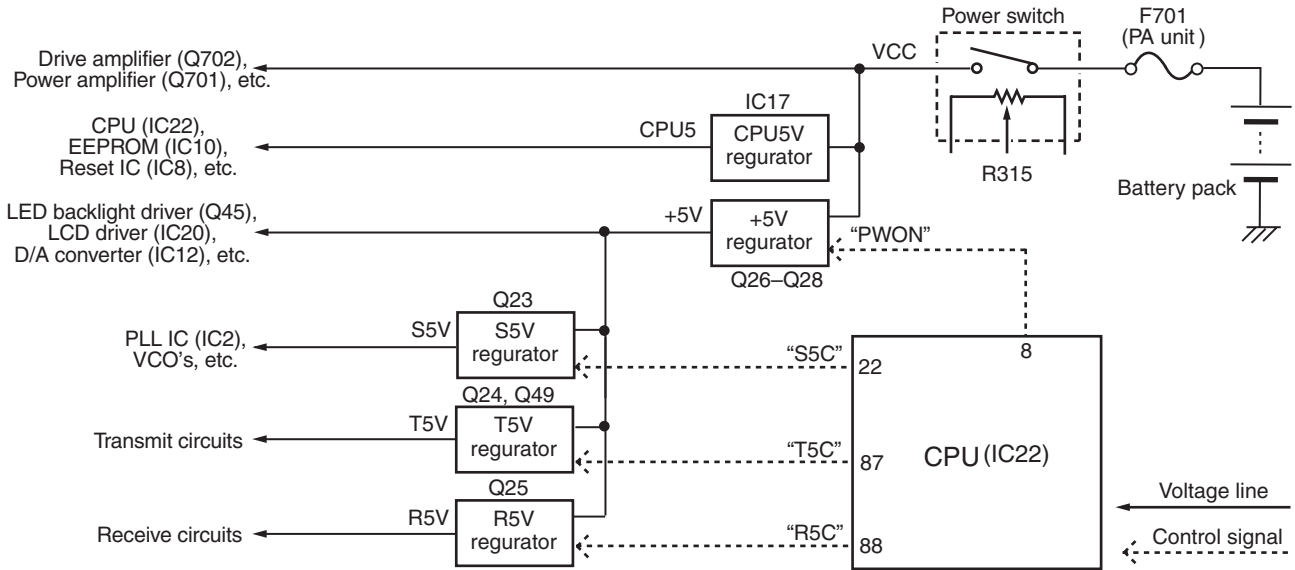
If the oscillated signal drifts, its phase changes from that of the reference frequency, causing a lock voltage change to compensate for the drift in the VCO oscillating frequency.

#### • PLL CIRCUITS



## 5-4 POWER SUPPLY CIRCUITS

Voltage from the attached battery pack is routed to whole of the circuit in the transceiver via a switch and regulators.



## 5-5 PORT ALLOCATIONS

### 5-5-1 CPU (IC22)

Pin No.	Port Name	Description
1-3	KR1-KR3	Input ports for dealer-programmable keys.
4-8	KS0-KS4	Output ports for dealer-programmable keys.
9	BUSY	Outputs "BUSY" signal to the DSP UNIT.
10	CCS	Outputs chip-select signal to the DSP UNIT.
11	SCK	Outputs serial clock signal to the PLL IC (IC2, pin 9) and D/A converter (IC12, pin 7/IC23, pin 7).
19-21	CENC0-CENC2	Output ports for CTCSS/DTCS signal.
22	S5C	Outputs S5V line control signal to the S5V regulator (Q23). "Low"=While power save mode.
26	T5C	Outputs T5V line control signal to the T5V line regulator (Q24, Q49). "Low"=While transmitting.
27	R5C	Outputs R5V line control signal to the R5V line regulator (Q25). "Low"=While receiving.
31	PLST	Outputs PLL strobe signal to the PLL IC (IC2, pin 11).
34	ULCK	Input port for PLL unlock detect signal from the PLL IC (IC2, pin 7). "Low"=While the PLL circuit is unlocked.
35	TMUT	Outputs transmit mute signal to the transmit mute switch (Q46). "High"=Transmitting is muted.
36	MONI	Input port for [SIDE1] key (S5). "Low"=While the key is pushed.
37	EMER	Input port for top switch (S1). "Low"=While the switch is pushed.
39	DSDA	Outputs serial data to the D/A converter (IC23, pin 6).
43	SENC	Outputs DTMF signals to the LPF (IC6, pin 3).
44	BEEP	Outputs beep sounds to the D/A converter (IC12, pin 21).
45	SDEC	Input port for DTMF signals.
46	CDEC	Input port for CTCSS/DTCS signals.
48	BATV	Input port for remaining battery power.
49	LVIN	Input port for VCO lock voltage.
50	RSSI	Input port for receive signal strength level signal from the FM IF IC (IC9, pin 12).
59	RES	Input port for CPU reset signal from the reset IC (IC8, pin 1). "Low"=When the CPU is reset.
69	CSFT	Outputs CPU clock shift signal to the clock shift switch (D6).
70	AFON	Outputs AF power amplifier (IC15) control signal to the AF power amplifier controller (Q41, Q42, D21, D23). "High"=The AF power amplifier is ON.
71	DAST	Outputs strobe signal to the D/A converter (IC12, pin 6).
72	DUSE	Outputs CTCSS/DTCS select signal to the tone filter switch (Q40). "High"=While DTCS is in use.
75	NOIS	Input port for noise signal from the FM IF IC (IC9, pin 13).
82	ESDA	Outputs serial data to the EEPROM (IC10, pin 5).
85	ESCL	Outputs clock signal to the EEPROM (IC10, pin 6).

Pin No.	Port Name	Description
89	DIGI	Outputs Analog/Digital select signal to the A/D switch (D2, D3). "High"=Digital mode is selected.
91	LSO	Outputs serial data to the LCD driver (IC20, pin 48).
92	LSCK	Outputs clock signal to the LCD driver (IC20, pin 47).
93	LCS	Outputs chip-enable signal to the LCD driver (IC20, pin 46).
94	LINH	Outputs display inhibit signal to the LCD driver (IC20, pin 45).
95	LIGT	Outputs LCD backlight control signal to the backlight LED's (DS1, DS2) driver (Q45). "Low"=While the backlight is ON.
96	RMUTE	Outputs AF mute signal to the analog switch (IC3, pins 12, 13). "Low"=While the squelch is close or transmitting.
97	MMUTE	Outputs MIC signals mute signal to the analog switch (IC3, pin 5) and MIC mute switch (Q31). "Low"=While receiving.
99	SSO	Outputs serial data to the PLL IC (IC2, 10) and D/A converter (IC12, pin 8).
100	KR0	Input ports for dealer-programmable keys.

### 5-5-2 D/A CONVERTER (IC12)

Pin No.	Port Name	Description
2	MCGO	Outputs level-adjusted MIC signals to the MIC mute switch (Q31).
10, 16	MOD	Outputs modulation signal to the modulation circuits (D2).
14	REF	Outputs reference frequency control voltage to the reference frequency oscillator buffer (IC7, pin 13).
15	BAL	Outputs modulation balance control signal to the reference frequency oscillator buffer (IC7, pin 12).
22	BEEPO	Outputs beep sounds to the AF volume buffer (IC6, pin 9).
23	SQLC	Outputs level-adjusted AF signals to the noise filter (IC9, pins 7, 8, R42, R44-R46, C69, C70, C413).

### 5-5-3 D/A CONVERTER (IC23)

Pin No.	Port Name	Description
1	T1	<ul style="list-style-type: none"> <li>• <b>While receiving</b> Outputs BPF tuning voltage to the tunable BPF (D19, D25, L7, L8, C21-C23, C25, C27-C29).</li> <li>• <b>While transmitting (as "APC" signal)</b> Outputs transmit mute signal to the transmit mute switch (Q46).</li> </ul>
2	T2	Outputs BPF tuning voltage to the tunable BPF (D14, D15, L9, L11, C19, C36, C39-C41, C44, C45).
3	TXLVA	Outputs oscillating frequency adjust voltage to the TX VCO (Q16, D10, D13).
4	RXLVA	Outputs oscillating frequency adjust voltage to the RX VCO (Q17, D9, D11).

# SECTION 6 ADJUSTMENT PROCEDURES

## 6-1 PREPARATION

When adjusting IC-F3020 series, CS-F3020 CLONING SOFTWARE, CS-F3020 ADJ ADJUSTMENT SOFTWARE (Rev. 1.0 or later), OPC-478/U JIG CABLE (modified OPC-478/U CLONING CABLE; see the page 6-2) and the following test equipments are required.

EQUIPMENT	GRADE AND RANGE	EQUIPMENT	GRADE AND RANGE
DC power supply	Output voltage : 7.2 V DC Current capacity : More than 1.5 A	External speaker	Input impedance : 8 Ω Capacity : 1 W or more
FM deviation meter	Frequency range : DC–300 MHz Measuring range : 0 to ±10 kHz	Attenuator	Power attenuation : 20 or 30 dB Capacity : 7.5 W
Frequency counter	Frequency range : 0.1–300 MHz Frequency accuracy : ±1 ppm or better Sensitivity : 100 mV or better	Standard signal generator (SSG)	Frequency range : 0.1–300 MHz Output level : 0.1 μV to 32 mV (–127 to –17 dBm)
RF power meter	Measuring range : 0.1–7.5 W Frequency range : 100–300 MHz Impedance : 50 Ω SWR : Better than 1.2 : 1	Oscilloscope	Frequency rang : DC–20 MHz Measuring range : 0.01–20 V

### ■ SYSTEM REQUIREMENTS (for the ADJUSTMENT SOFTWARE)

- Microsoft® Windows® 98/98SE/Me/2000/XP
- RS-232C serial port (D-sub 9 pin) or USB port

### ■ ADJUSTMENT SOFTWARE INSTALLATION

- ① Quit all applications when Windows is running.
- ② Insert the CD into the appropriate CD drive.
- ③ Double-click the “Setup.exe” contained in the ‘CS-F3020 ADJ’ folder in the CD drive.
- ④ The “Welcome to the InstallShield Wizard for CS-F3020 ADJ” will appear. Click [Next>].
- ⑤ The “Choose Destination Location” will appear. Then click [Next>] to install the software to the destination folder. (e.g. C:\Program Files\Icom\CS-F3020 ADJ)
- ⑥ After the installation is completed, the “InstallShield Wizard Complete” will appear. Then click [Finish].
- ⑦ Eject the CD.
- ⑧ Program group ‘CS-F3020 ADJ’ appears in the ‘Programs’ folder of the start menu, and ‘CS-F3020 ADJ’ icon appears on the desk top screen.

### ■ BEFORE STARTING SOFTWARE ADJUSTMENT

Clone the adjustment frequencies and settings into the transceiver, and set the configuration using the CS-F3020 CLONING SOFTWARE before starting the software adjustment. Otherwise, the software adjustment can not be started.

**CAUTION!:** **BACK UP** the originally programmed memory data in the transceiver before programming the adjustment frequencies. When program the adjustment frequencies into the transceiver, the transceiver’s memory data will be overwritten and lose original memory data at the same time.

Microsoft and Windows are registered trademarks of Microsoft Corporation in the U.S.A. and other countries.

### ■ STARTING SOFTWARE ADJUSTMENT

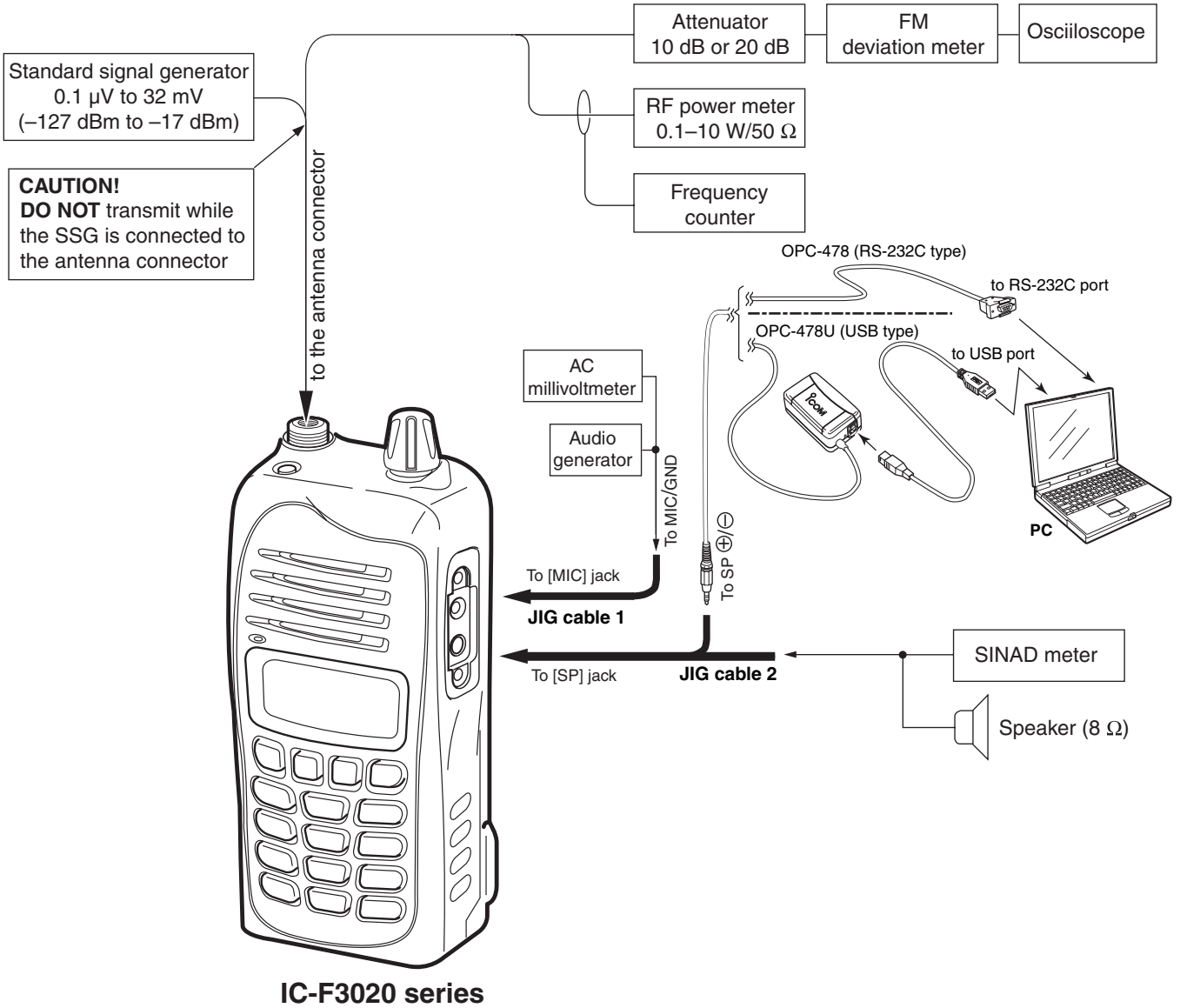
- ① Connect the transceiver and PC with OPC-478/U JIG CABLE.
- ② Turn the transceiver power ON.
- ③ Boot up Windows, and click the program group ‘CS-F3020 ADJ’ in the ‘Programs’ folder of the [Start] menu, then CS-F3020 ADJ’s window appears.
- ④ Click ‘Connect’ on the CS-F3020 ADJ’s window, then the window shows transceiver’s condition and adjustment items as below.
- ⑤ Set or modify adjustment data as specified.

### • ADJUSTMENT FREQUENCY LIST

CH	FREQUENCY	ADJUSTMENT ITEM
1	174.000 MHz	TX power : Low1 Mode : Narrow
2	136.000 MHz	TX power : Low1 Mode : Wide
3	155.000 MHz	TX power : High Mode : Wide
4	155.000 MHz	TX power : Low2 Mode : Wide
5	155.000 MHz	TX power : Low1 Mode : Narrow
6	155.000 MHz	TX power : Low1 Mode : Narrow DTCS : 007
7*	155.000 MHz	TX power : Low1 Mode : Middle
8	155.000 MHz	TX power : Low1 Mode : Wide CTCSS : 151.4 Hz
9	155.000 MHz	TX power : Low1 Mode : Wide

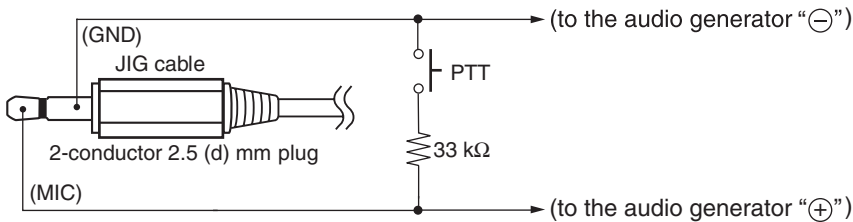
\*; [EUR] only

• CONNECTION

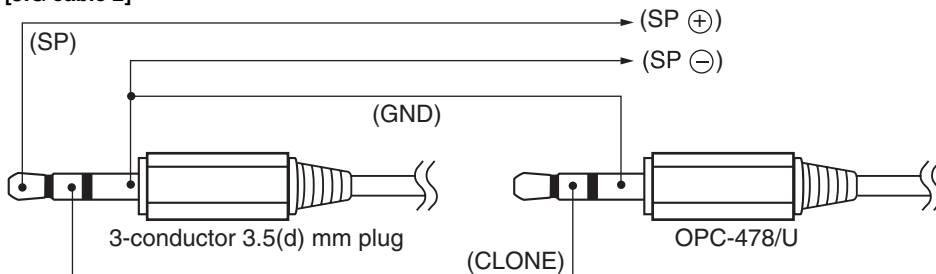


• JIG CABLES

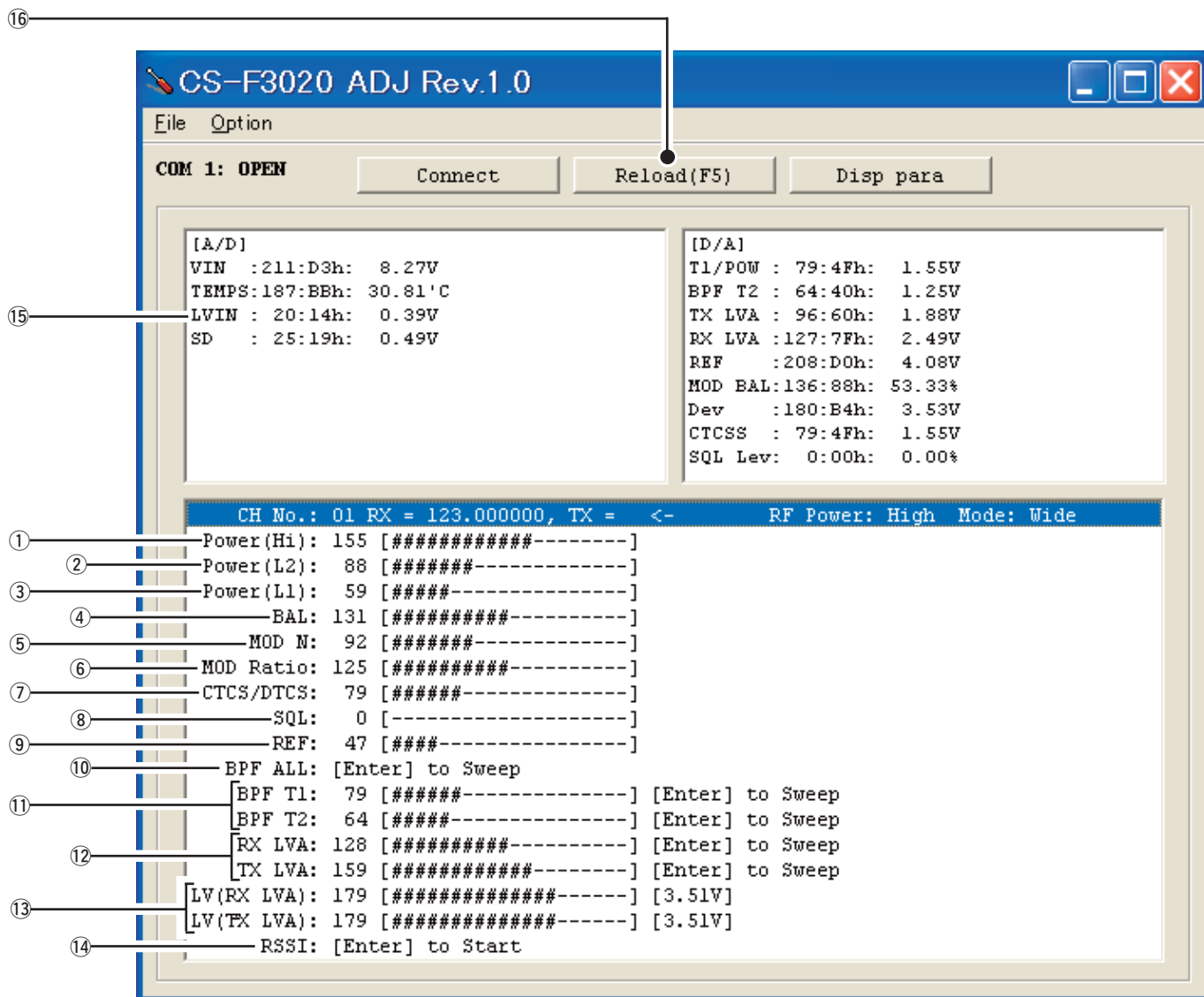
[JIG cable 1]



[JIG cable 2]



• PC SCREEN EXSAMPLE



**NOTE:** The above values for settings are example only.  
Each transceiver has its own specific values for each setting.

- |                               |                                       |
|-------------------------------|---------------------------------------|
| ① Transmit output power (Hi)  | ⑨ Reference frequency                 |
| ② Transmit output power (L2)  | ⑩ Receive sensitivity (Automatically) |
| ③ Transmit output power (L1)  | ⑪ Receive sensitivity (Manually)      |
| ④ Modulation balance          | ⑫ PLL lock voltage                    |
| ⑤ FM deviation (Narrow)       | ⑬ PLL lock voltage preset             |
| ⑥ FM deviation (Middle*/Wide) | ⑭ S-meter                             |
| ⑦ CTCSS/DTCS deviation        | ⑮ PLL lock voltage (verify)           |
| ⑧ Squelch                     | ⑯ Reload data                         |

\*; [EUR] only



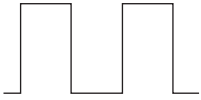
## 6-2 FREQUENCY ADJUSTMENT

Select an adjustment item using [↑]/[↓] keys, then set to the specified value using [←-]/[→] keys on the connected PC's keyboard.

ADJUSTMENT	ADJUSTMENT CONDITION	MEASUREMENT		VALUE
		UNIT	OPERATION	
PLL LOCK VOLTAGE [RX LVA]	1 • Channel : CH 1 • Receiving	PC screen	Click [Reload (F5)] button, then check the "LVIN" item on the CS-F3020 ADJ's screen as below.	3.5 V
[TX LVA]	2 • Channel : CH 1 • Transmitting			
<p><b>CONVENIENT:</b> The "PLL LOCK VOLTAGE" can be adjusted automatically.            1: Set the Lock voltage preset ([RX LVA] and [TX LVA]) to "179 (3.15 V)."            2: Push the [ENTER] key on the connected PC's keyboard.</p>				
	3 • Channel : CH 2 • Receiving	PC screen	Click [Reload (F5)] button, then check the "LVIN" item on the CS-F3020 ADJ's screen.	0.9–1.5 V (Verify)
	4 • Channel : CH 2 • Transmitting			1.0–1.6 V (Verify)
REFERENCE FREQUENCY [REF]	1 • Channel : CH 1 • Connect an RF power meter to the antenna connector. • Transmitting	Top panel	Loosely couple a frequency counter to the antenna connector.	174.000000 MHz

### 6-3 TRANSMIT ADJUSTMENT

Select an adjustment item using [↑]/[↓] keys, then set to the specified value using [←]/[→] keys on the connected PC's keyboard.

ADJUSTMENT	ADJUSTMENT CONDITION	MEASUREMENT		VALUE
		UNIT	OPERATION	
OUTPUT POWER [Power (Hi)]	1 • Channel : CH 3 • Transmitting	Top panel	Connect an RF power meter to the antenna connector.	5.0 W
[Power (L2)]	2 • Channel : CH 4 • Transmitting			2.0 W
[Power (L1)]	3 • Channel : CH 5 • Transmitting			1.0 W
MODULATION BALANCE [BAL]	1 • Channel : CH 6 • No audio applied to the JIG cable. • Set an FM deviation meter same as; HPF : OFF LPF : 20 kHz De-emphasis : OFF Detector : (P-P)/2 • Transmitting	Top panel	Connect the FM deviation meter to the antenna connector through an attenuator.	Set to square wave form 
FM DEVIATION (NARROW) [MOD N]	1 • Channel : CH 5 • Connect an audio generator to the JIG cable and set as; Frequency : 1.0 kHz Level : 150 mV rms • Set the FM deviation meter to same condition as "MODULATION BALANCE." • Transmitting	Top panel	Connect the FM deviation meter to the antenna connector through an attenuator.	±2.05 to ±2.15 kHz
(WIDE) [MOD ratio]	2 • Channel : CH 9 • Transmitting			±4.05 to ±4.15 kHz
(MIDDLE)* [MOD ratio]	3 • Channel : CH 7 • Transmitting			±3.15 to ±3.25 kHz
CTCSS/DTCS DEVIATION [CTCS/DTCS]	1 • Channel : CH 8 • No audio applied to the JIG cable. • Set the FM deviation meter to same condition as "MODULATION BALANCE." • Transmitting	Top panel	Connect an FM deviation meter to the antenna connector through an attenuator.	±0.68 to ±0.72 kHz

\*; [EUR] only.

## 6-4 RECEIVE ADJUSTMENT

Select an adjustment item using [↑] / [↓] keys, then set to the specified value using [←] / [→] keys on the connected PC's keyboard.

ADJUSTMENT	ADJUSTMENT CONDITION	MEASUREMENT		VALUE	
		UNIT	LOCATION		
RECEIVE SENSITIVITY [BPF (T1)] [BPF (T2)]	<b>NOTE:</b> "RECEIVE SENSITIVITY" must be adjusted before "S-METER." Otherwise, "S-METER" will not be adjusted properly.				
	1	<ul style="list-style-type: none"> <li>• Channel : CH 2</li> <li>• Connect the SSG to the antenna connector and set as;                             <ul style="list-style-type: none"> <li>Frequency : 136.000 MHz</li> <li>Level : +20 dBμ<sup>†</sup> (-87 dBm)</li> <li>Modulation : 1 kHz</li> <li>Deviation : ±3.5 kHz</li> </ul> </li> <li>• Receiving</li> </ul>	[MIC/SP] jack	Connect the SINAD meter with an 8 Ω load to the JIG cable.	Minimum distortion level
<b>CONVENIENT:</b> The "RECEIVE SENSITIVITY" can be adjusted automatically. 1: Put the cursor on "BPF ALL" and push [ENTER] key. 2: The connected PC tunes BPF's to peak levels automatically.					
S-METER [RSSI]	1	<ul style="list-style-type: none"> <li>• Channel : CH 2</li> <li>• Connect the SSG to the antenna connector and set as;                             <ul style="list-style-type: none"> <li>Frequency : 136.000 MHz</li> <li>Level : +23 dBμ<sup>†</sup> (-84 dBm)</li> <li>Modulation : 1 kHz</li> <li>Deviation : ±3.5 kHz</li> </ul> </li> <li>• Receiving</li> </ul>	Push the [ENTER] key on the connected PC's keyboard to set "S3" level.		
	2	<ul style="list-style-type: none"> <li>• Set the SSG as;                             <ul style="list-style-type: none"> <li>Level : -7 dBμ<sup>†</sup> (-114 dBm)</li> </ul> </li> <li>• Receiving</li> </ul>	Push the [ENTER] key on the connected PC's keyboard to set "S1" level.		
SQUELCH [SQL]	1	<ul style="list-style-type: none"> <li>• Channel : CH 9</li> <li>• Close the squelch by adjusting the value of [SQL] item on the CS-F3020 ADJ's screen.</li> <li>• Connect the SSG to the antenna connector and set as;                             <ul style="list-style-type: none"> <li>Frequency : 155.000 MHz</li> <li>Level : -15 dBμ<sup>†</sup> (-122 dBm)</li> <li>Modulation : 1 kHz</li> <li>Deviation : ±3.5 kHz</li> </ul> </li> <li>• Receiving</li> </ul>	External speaker	Connect an 8 Ω speaker to the JIG cable.	Set the [SQL] to the value that the audio signals just appears.

<sup>†</sup>The output level of the standard signal generator (SSG) is indicated as the SSG's open circuit.





[MAIN UNIT]

Table with columns: REF NO., ORDER NO., DESCRIPTION, M., H/V LOCATION. Contains 160 rows of data for various units.

[MAIN UNIT]

Table with columns: REF NO., ORDER NO., DESCRIPTION, M., H/V LOCATION. Contains 314 rows of data for various units.

[A]=USA-02 (F3021T) [B]=EUR-02 (F3022T) [C]=GEN-02 (F3023T) [D]=USA-02 (F3021S) [E]=EUR-02 (F3022S) [F]=GEN-02 (F3023S)

M.=Mounted side (T: Mounted on the Top side, B: Mounted on the Bottom side) S.=Surface mount



**[PA UNIT]**

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
C701	4030017460	S.CER ECJ0EB1E102K	T	35/11
C702	4030017430	S.CER ECJ0EC1H101J	B	33.8/10
C703	4030017420	S.CER ECJ0EC1H470J	B	34.8/10
C704	4030017390	S.CER ECJ0EC1H180J	B	32.3/10
C705	4030007040	S.CER C1608 CH 1H 180J-T	T	31.3/14.5
C706	4030007050	S.CER C1608 CH 1H 220J-T	T	29.7/14
C707	4030017460	S.CER ECJ0EB1E102K	T	26.8/17.5
C708	4030017510	S.CER ECJ0EC1H680J	T	27.9/15.2
C709	4030017460	S.CER ECJ0EB1E102K	T	21.4/17.5
C710	4030017460	S.CER ECJ0EB1E102K	T	21.7/22.7
C711	4030007100	S.CER C1608 CH 1H 560J-T	T	26.2/14
C713	4030017460	S.CER ECJ0EB1E102K	B	31.3/10.3
C715	4030017420	S.CER ECJ0EC1H470J	T	15.7/13.5
C716	4030016790	S.CER ECJ0EB1C103K	T	14.7/13.5
C718	4030017460	S.CER ECJ0EB1E102K	B	18/5.1
C719	4030017460	S.CER ECJ0EB1E102K	T	13.7/13.5
C720	4030017680	S.CER ECJ0EC1H820J	B	19/8.4
C722	4030017460	S.CER ECJ0EB1E102K	B	14/12.9
C723	4030017460	S.CER ECJ0EB1E102K	B	10.8/10.9
C724	4030017460	S.CER ECJ0EB1E102K	T	19.4/18.1
C725	4030017460	S.CER ECJ0EB1E102K	T	7.2/14
C727	4030017460	S.CER ECJ0EB1E102K	T	9.9/14
C728	4030017380	S.CER ECJ0EC1H050B	T	6.3/14
C729	4030017640	S.CER ECJ0EC1H150J	B	12/12.1
C731	4030017380	S.CER ECJ0EC1H050B	T	5.2/8.3
C732	4030017460	S.CER ECJ0EB1E102K	T	11.2/18.7
C733	4030017420	S.CER ECJ0EC1H470J	B	13.9/20.2
C734	4030017460	S.CER ECJ0EB1E102K	T	16.3/2.5
C742	4030017460	S.CER ECJ0EB1E102K	T	34.1/14.2
C744	4030017640	S.CER ECJ0EC1H150J	T	31.6/16.9
C745	4030017550	S.CER ECJ0EC1H1R5B	T	35.5/16.5
C746	4030017410	S.CER ECJ0EC1H240J	T	31.3/18.4
C748	4030018860	S.CER ECJ0EB0J105K	B	14.9/12.9
C750	4030018120	S.CER ECJ0EC1H110J	B	32/15
C751	4030017630	S.CER ECJ0EC1H120J	B	32/16.7
C752	4030017460	S.CER ECJ0EB1E102K	B	26.6/17.9
C753	4030017460	S.CER ECJ0EB1E102K	B	26.8/16.4
C754	4030017460	S.CER ECJ0EB1E102K	B	29.2/12.5
C755	4030017420	S.CER ECJ0EC1H470J	B	16.7/12.9
C756	4030017420	S.CER ECJ0EC1H470J	B	15.8/12.9
C757	4030017460	S.CER ECJ0EB1E102K	T	16.7/13.5
C758	4030017420	S.CER ECJ0EC1H470J	T	16.3/1.6
C759	4030017460	S.CER ECJ0EB1E102K	B	7.6/10.9
C760	4030017460	S.CER ECJ0EB1E102K	B	21.2/19.9
C761	4030017460	S.CER ECJ0EB1E102K	T	7.2/9.9
C763	4030016790	S.CER ECJ0EB1C103K	T	8.1/14
C767	4030017460	S.CER ECJ0EB1E102K	B	16.7/14.5
C768	4030017520	S.CER ECJ0EC1H0R3B	T	29.4/15.8
C769	4030017520	S.CER ECJ0EC1H0R3B	B	34.2/16.8
C780	4030017460	S.CER ECJ0EB1E102K	B	15.7/22.6
C781	4030016930	S.CER ECJ0EB1A104K	B	16.7/24.2
C782	4030017460	S.CER ECJ0EB1E102K	B	14.7/23.7
C784	4030017420	S.CER ECJ0EC1H470J	B	19.5/20.7
C785	4030016790	S.CER ECJ0EB1C103K	B	21.2/23.7
C786	4550007600	S.TAN F920J106MPABMA	T	17.7/21.1
C787	4030017580	S.CER ECJ0EC1H060C	T	26.2/20.1
J701	6910017680	CNR IMSA-9230B-1-04Z140-PT1		
J702	6910017680	CNR IMSA-9230B-1-04Z140-PT1		
F701	5210000901	S.FUS 0467003.NR (0434003)	B	12.7/20.2
EP711	6910015370	S.BEA ACZ1005Y-102-T	T	9/14
EP712	6910012350	S.BEA MMZ1608Y 102BT	B	14.6/14.4

**[CONNECT UNIT]**

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
L901	6200006190	S.COL BLM21PG300SN1D	T	8.7/6
C901	4030017460	S.CER ECJ0EB1E102K	T	5.6/6
C902	4030017420	S.CER ECJ0EC1H470J	T	5.6/7
J901	6910015881	CNR 9230B-1-02Z141-PT1		

M.=Mounted side (T: Mounted on the Top side, B: Mounted on the Bottom side)  
S.=Surface mount

**[ANT UNIT]**

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
L801	6200008580	S.COL 0.30-1.4-6TL 32N	T	4.2/12.3
L802	6200008280	S.COL 0.30-1.7-7TL 50N	T	3.6/8.6
R801	7030005080	S.RES ERJ2GEJ 823 X (82 k)	T	9/13.5
C801	4030017460	S.CER ECJ0EB1E102K	T	9.6/7.4
C802	4030017380	S.CER ECJ0EC1H050B	T	5.5/8.6
C803	4030017410	S.CER ECJ0EC1H240J	T	1.8/14.1
C807	4030017620	S.CER ECJ0EC1H100C	T	3.4/14.1

[A]=USA-02 (F3021T) [B]=EUR-02 (F3022T) [C]=GEN-02 (F3023T)  
[D]=USA-02 (F3021S) [E]=EUR-02 (F3022S) [F]=GEN-02 (F3023S)



• BC-160 (Optional product)

[MAIN UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
IC1	1110006480	S.IC NJM2801U1-0543-TE1	B	10.2/62.3
IC2	1110003071	S.IC μPC494GS-E1-A	B	13/36.7
IC3	1140012301	S.IC μPD78912AMC-534-5A4-A	B	19.1/19.6
IC4	1110002700	S.IC NJM2904M-TE1	B	35.1/13.8
Q1	1530002060	S.TR 2SC4081 T106 R	B	37.3/63.9
Q2	1550000090	S.FET RSQ035P03TR	B	37.3/60.9
Q3	1530002060	S.TR 2SC4081 T106 R	B	35.8/19.3
Q4	1530002060	S.TR 2SC4081 T106 R	B	41.6/18.3
Q5	1590000430	S.TR DTC144EUA T106	B	23.7/26.6
Q6	1530002060	S.TR 2SC4081 T106 R	B	22.5/58.2
D1	1730002350	S.ZEN MA8110-M (TX)	B	17.6/60.5
D2	1750000550	S.DIO 1SS355 TE-17	B	34.8/64.4
D3	1750001110	S.DIO SM240A-T	B	44.4/56
D4	1160000070	S.DIO DAN202K T146	B	26.9/20.6
D5	1750000550	S.DIO 1SS355 TE-17	B	27.2/15
X1	6060000790	S.CER CSTCR4M91G	B	10.3/17.7
L1	6190001640	S.COL SLF12555T-101M1R1	B	35.3/52.7
L2	6200002611	S.COL NLV25T-F47J	B	20.8/28.2
R2	7030000460	S.RES MCR10EZHZJ 4.7 k	B	20.3/58.6
R3	7030003410	S.RES ERJ3GEYJ 561 V (560)	B	37.3/65.8
R4	7030003200	S.RES ERJ3GEYJ 100 V (10)	B	34.6/61.1
R5	7030009580	S.RES ERJ8RSJ R12V	B	31.8/73
R6	7030000540	S.RES MCR10EZHZJ 22 k	B	30.1/26.8
R7	7030000380	S.RES MCR10EZHZJ 1 k	B	30.1/29.6
R8	7030003520	S.RES ERJ3GEYJ 472 V (4.7 k)	B	39.5/18.4
R9	7030003600	S.RES ERJ3GEYJ 223 V (22 k)	B	13.8/30.5
R10	7030000740	S.RES MCR10EZHZJ 1 M	B	25.8/34.9
R11	7030000540	S.RES MCR10EZHZJ 22 k	B	24.8/37.8
R12	7030003560	S.RES ERJ3GEYJ 103 V (10 k)	B	10.8/30.5
R13	7030003600	S.RES ERJ3GEYJ 223 V (22 k)	B	19.3/34.5
R14	7030003770	S.RES ERJ3GEYJ 564 V (560 k)	B	21/34.5
R15	7030003650	S.RES ERJ3GEYJ 563 V (56 k)	B	21/40.3
R16	7030003770	S.RES ERJ3GEYJ 564 V (560 k)	B	19.3/40.3
R17	7030003560	S.RES ERJ3GEYJ 103 V (10 k)	B	22.7/40.3
R18	7030003410	S.RES ERJ3GEYJ 561 V (560)	B	8.6/43.5
R19	7030003620	S.RES ERJ3GEYJ 333 V (33 k)	B	16.7/42.1
R20	7030000560	S.RES MCR10EZHZJ 33 k	B	28.1/40.4
R21	7030000380	S.RES MCR10EZHZJ 1 k	B	26.3/28.6
R22	7030000440	S.RES MCR10EZHZJ 3.3 k	B	24/11.4
R23	7030000460	S.RES MCR10EZHZJ 4.7 k	B	24/8.8
R24	7030000260	S.RES MCR10EZHZJ 100 (101)	B	20.3/11.4
R25	7030000260	S.RES MCR10EZHZJ 100 (101)	B	20.3/8.8
R26	7030000500	S.RES MCR10EZHZJ 10 k	B	30.5/22
R27	7030007220	S.RES ERA3YED 202V (2 k)	B	30.2/19.6
R28	7030011200	S.RES ERA3YEB 303V (30 k)	B	30/18.1
R29	7030011190	S.RES ERA3YEB 103V (10 k)	B	26.9/16.6
R30	7030005871	S.RES ERA3YKD 104V (100 k)	B	40.4/15.1
R31	7030003560	S.RES ERJ3GEYJ 103 V (10 k)	B	29.8/15.9
R32	7030005341	S.RES ERA3YED 332V (3.3 k)	B	43.4/11.1
R33	7030000500	S.RES MCR10EZHZJ 10 k	B	31.7/9.8
R34	7030000740	S.RES MCR10EZHZJ 1 M	B	42.1/14.4
R35	7030003440	S.RES ERJ3GEYJ 102 V (1 k)	B	40.4/12.2
R36	7030000460	S.RES MCR10EZHZJ 4.7 k	B	34.7/23.8
R37	7030005501	S.RES ERA3YKD 124V (120 k)	B	33.6/21.3
R38	7030005671	S.RES ERA3YKD 393V (39 k)	B	30.1/25.1
R39	7030000010	S.RES MCR10EZHZJ JPW	B	28.1/42.2
R40	7030000010	S.RES MCR10EZHZJ JPW	B	27.7/8.4
R41	7030000010	S.RES MCR10EZHZJ JPW	B	34.6/9.2
R42	7030008240	S.RES ERJ12YJ0R00U	B	38.9/7.4
R43	7030000010	S.RES MCR10EZHZJ JPW	B	30.1/33.3
R44	7030000010	S.RES MCR10EZHZJ JPW	B	28.1/33.3
R45	7030000010	S.RES MCR10EZHZJ JPW	B	29.1/38
R46	7030000010	S.RES MCR10EZHZJ JPW	B	29.1/36.1
R47	7030000010	S.RES MCR10EZHZJ JPW	B	43.9/18.3
R48	7030008240	S.RES ERJ12YJ0R00U	B	43/28.9
R49	7030000010	S.RES MCR10EZHZJ JPW	B	23.5/5.7
R50	7030008240	S.RES ERJ12YJ0R00U	B	34.1/38.2
R51	7030000010	S.RES MCR10EZHZJ JPW	B	23.9/34.9
R52	7030000010	S.RES MCR10EZHZJ JPW	B	24.7/32
R53	7030008240	S.RES ERJ12YJ0R00U	B	38.6/33.1
R54	7030000010	S.RES MCR10EZHZJ JPW	B	34.4/33
R55	7030000010	S.RES MCR10EZHZJ JPW	B	32/60.1
R56	7030000010	S.RES MCR10EZHZJ JPW	B	42.1/41.5
R57	7030000010	S.RES MCR10EZHZJ JPW	B	37.2/41.5
R58	7030000010	S.RES MCR10EZHZJ JPW	B	24.9/57
R59	7030000010	S.RES MCR10EZHZJ JPW	B	27.6/49.3
R60	7030000010	S.RES MCR10EZHZJ JPW	B	16.3/58.3
R61	7030000010	S.RES MCR10EZHZJ JPW	B	4.9/31.7
R62	7030000010	S.RES MCR10EZHZJ JPW	B	39.1/41.5
R63	7030000010	S.RES MCR10EZHZJ JPW	B	31.4/43.6

[MAIN UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
R64	7030000010	S.RES MCR10EZHZJ JPW	B	40.1/38.7
R65	7030000010	S.RES MCR10EZHZJ JPW	B	4.9/49.5
R66	7030003560	S.RES ERJ3GEYJ 103 V (10 k)	B	8.2/13.6
R67	7030000100	S.RES MCR10EZHZJ 4R7 (4.7)	B	10.2/45.1
C1	4030006900	S.CER C1608 JB 1H 103K-T	B	44.5/70.7
C2	4030006900	S.CER C1608 JB 1H 103K-T	B	48.2/73.5
C3	4030006860	S.CER C1608 JB 1H 102K-T	B	44.5/67.8
C4	4030006900	S.CER C1608 JB 1H 103K-T	B	32.9/69.7
C5	4030006900	S.CER C1608 JB 1H 103K-T	B	32.9/71.1
C6	4510008540	S.ELE EEE1CA100SR	B	17.5/63.8
C7	4030011600	S.CER C1608 JB 1E 104K-T	B	13.4/60.8
C8	4030006900	S.CER C1608 JB 1H 103K-T	B	9.2/58.7
C9	4030011600	S.CER C1608 JB 1E 104K-T	B	13.4/63.8
C10	4510009150	S.ELE EEE1EA470WP	B	43.6/62.8
C11	4030006900	S.CER C1608 JB 1H 103K-T	B	35.8/67.9
C12	4030006900	S.CER C1608 JB 1H 103K-T	B	32.4/67.9
C13	4510009150	S.ELE EEE1EA470WP	B	26.5/62.8
C14	4510008660	S.ELE EEE0JA220SR	B	34.3/28
C15	4510008660	S.ELE EEE0JA220SR	B	40.8/23.8
C16	4030006860	S.CER C1608 JB 1H 102K-T	B	12.3/30.5
C17	4030011600	S.CER C1608 JB 1E 104K-T	B	19.3/37.4
C18	4030006900	S.CER C1608 JB 1H 103K-T	B	21/37.4
C19	4030006900	S.CER C1608 JB 1H 103K-T	B	13.1/42.2
C20	4030009980	S.CER C1608 JB 1H 152K-T	B	8.6/42.1
C21	4030011600	S.CER C1608 JB 1E 104K-T	B	9.5/21.9
C22	4030006900	S.CER C1608 JB 1H 103K-T	B	19/25.5
C23	4030006900	S.CER C1608 JB 1H 103K-T	B	26.9/23.2
C24	4030006900	S.CER C1608 JB 1H 103K-T	B	26.9/18
C25	4030004760	S.CER C2012 JF 1H 104Z-T	B	33.2/17.5
C26	4030006900	S.CER C1608 JB 1H 103K-T	B	41.9/11.1
C27	4030006900	S.CER C1608 JB 1H 103K-T	B	29.5/12.6
C28	4030006900	S.CER C1608 JB 1H 103K-T	B	30.1/23.7
J1	6510024940	CNR HEC2305-016250		
DS1	5040002740	LED RT3-03HRYG		

M.=Mounted side (T: Mounted on the Top side, B: Mounted on the Bottom side)  
S.=Surface mount

# SECTION 8 MECHANICAL PARTS AND DISASSEMBLY

## [CHASSIS PARTS]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
J1	6910015910	Connector ANT connector-104	1
J2	6910015860	Connector IMSA-6277S-02A-G	1
SP1	2510001060	Speaker K036NA500-47	1
W1	8900009640	Cable OPC-963	1
MP1	8010020410	2927 chassis	1
MP2	8210022970	2927 T-front panel assembly [10-key]	1
	8210022980	2927 S-front panel assembly [4-key]	1
MP8	8210020550	2721 rear panel	1
MP9	8310066450	2927 window plate	1
MP10	8930069510	2927 window sheet	1
MP12	8930069490	2927 keyboard [10-key]	1
	8930069700	2927 4-key [4-key]	1
MP13	8930069500	2927 main seal	1
MP14	8930063060	2721 T-rubber	1
MP16	8930069460	2927 side plate	1
MP17	8930069470	2927 top plate	1
MP20	8930061880	2721 MIC sponge	1
MP21	8930059360	2600 release button	1
MP22	8930070360	2775 release plate (A)	1
MP24	8610012970	Knob N350	1
MP25	8830002900	2927 ANT nut	1
MP26	8810009561	Screw PH BT M2 x 6 NI-ZK3	2
MP27	8810009221	Screw PH BT M2 x 8 NI-ZK3	2
MP28	8810008641	Screw FH BT M2 x 4 NI-ZC3	11
MP31	8810010430	Screw trass M3 x 5 SUS SSBC	1
MP32	8310066810	2927 option plate	1
MP33	8930046020	1123 sheet (A)-1	1
MP34	8930056540	Push spring (AH)	2
MP35	8830001700	VR nut (Q)	1
MP36	8930048870	2056 A-sponge	1
MP37	8930070010	2893 VOL rubber	1

## [MAIN UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
J2	6450002250	Connector HSJ1456-010320	1
J3	6450000131	Connector HSJ1102-018540	1
R315	7210003061	Variable resistor TP76N00N-15F-A103-2251A	1
DS3	5030002760	LCD L3-0200HAY-3	1
MC1	7700002480	Microphone SKB-2746 LPC	1
S2	2260002840	Switch SKHLLFA010	1
EP7	8930063020	LCD contact SRCN-2721-SP-N-W	2
MP1	8510016130	2721 VCO case	1
MP2	8510016120	2721 VCO cover	1
MP3	8930069480	2927 LCD holder	1
MP4	8210020570	2721 reflector	1
MP5	8930070080	White sheet (V)	1
MP6	8510017720	2927 main shield	1
MP9	8930070700	Insulation sheet (LQ)	1

## [PA UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
J701	6910017680	Connector IMSA-9230B-1-04Z140-PT1	1
J702	6910017680	Connector IMSA-9230B-1-04Z140-PT1	1
MP701*	8410002530	2681 PA heatsink	1

## [ANT UNIT]

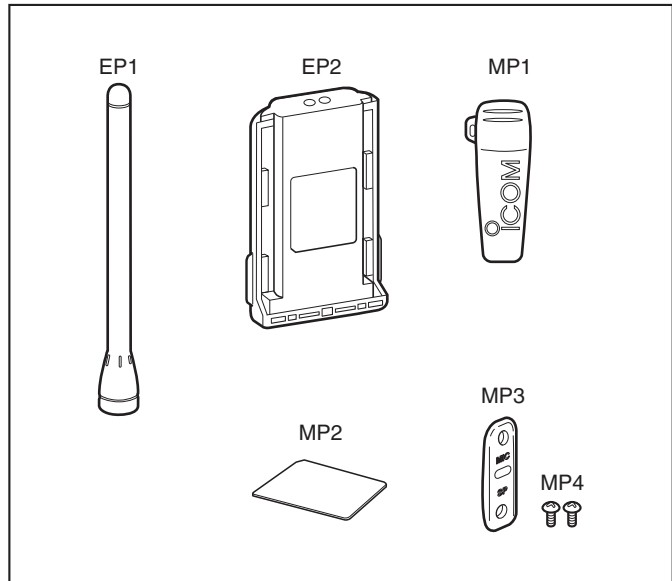
REF. NO.	ORDER NO.	DESCRIPTION	QTY.
MP801	8510017640	2927 ANT plate	1

## [CONNECT UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
J901	6910015881	Connector 9230B-1-02Z141-PT1	1

## [ACCESSORIES]

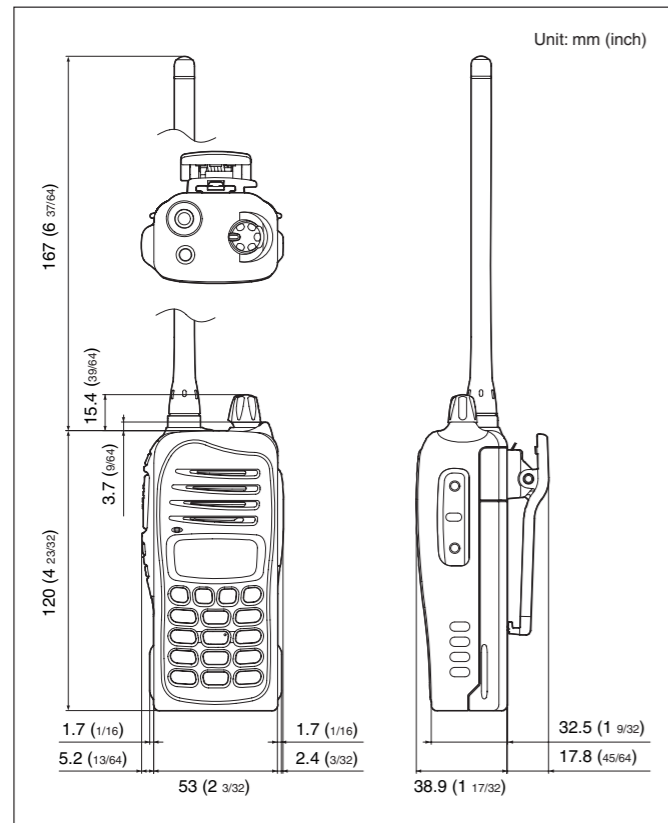
REF. NO.	ORDER NO.	DESCRIPTION	QTY.
EP1	Optional product	Antenna FA-SC55V-1	1
EP2	Optional product	Battery pack BP-231	1
MP1	Optional product	Belt clip MB-94	1
MP2	8310066810	2927 option plate	1
MP3	8210022780	2927 jack panel	1
MP4	8810004861	Screw PH M2 x 6 ZK3	2



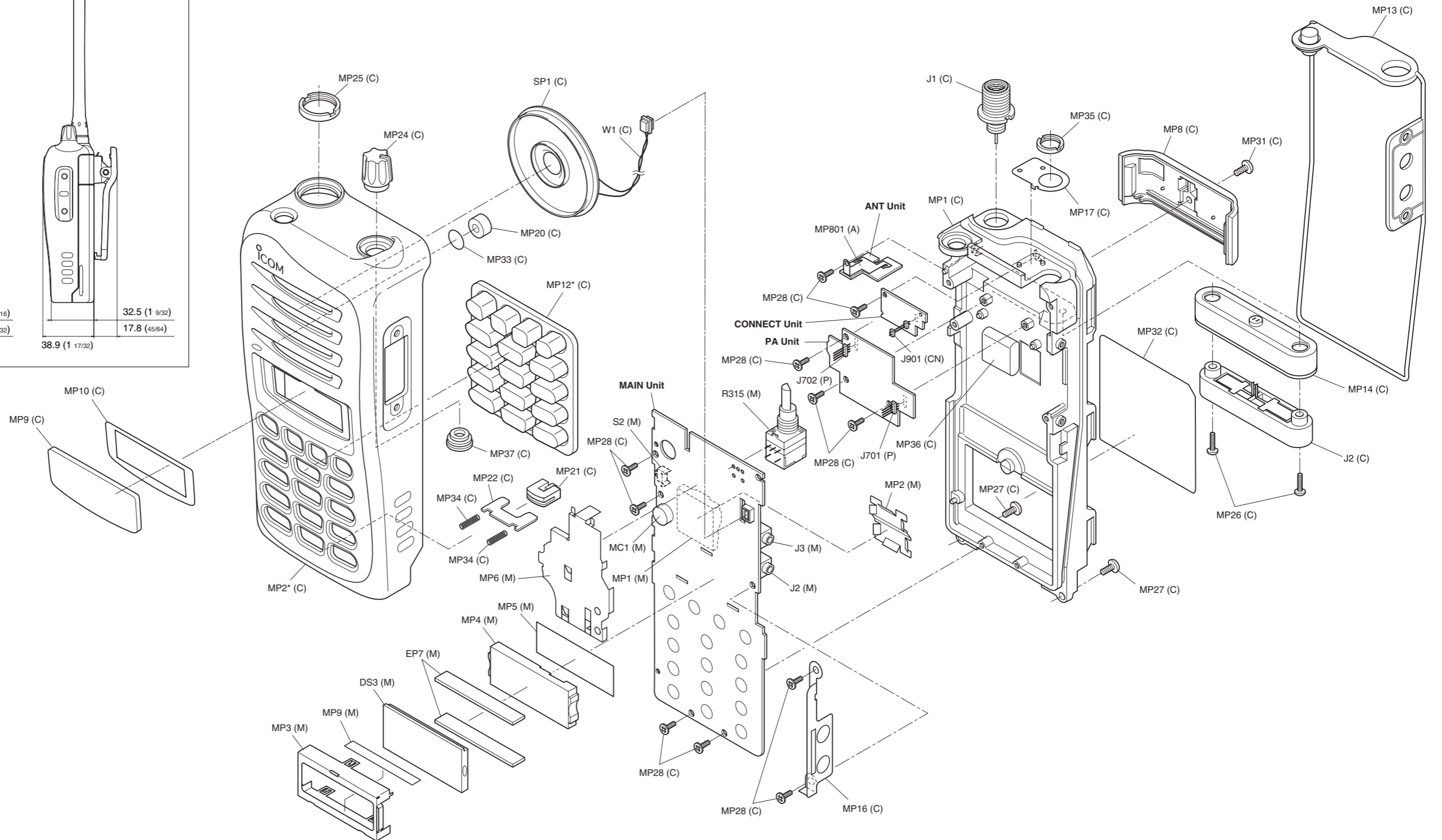
\*: Refer to SECTION 10 BOARD LAYOUTS.

### Screw abbreviations

BT: Self-tapping      PH: Pan head  
 ZK, ZK3: Black      FH: Flat head  
 NI: Nickel            SUS: Stainless



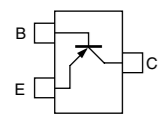
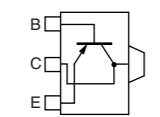
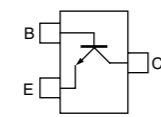
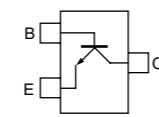
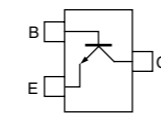
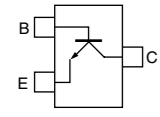
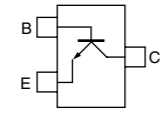
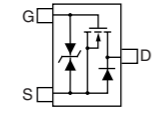
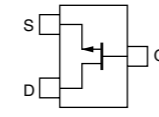
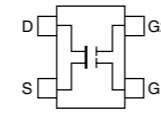
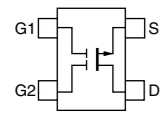
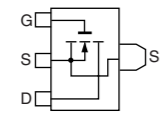
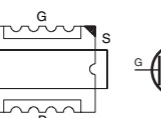
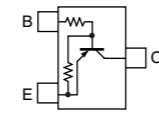
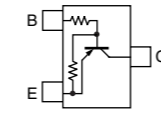
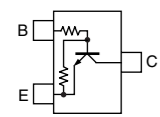
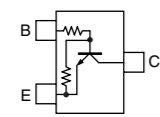
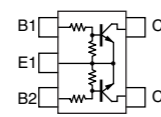
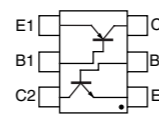
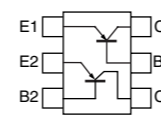
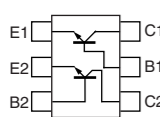
\* MP2 (C), MP12 (C): The shape is depending on the version.



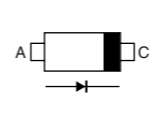
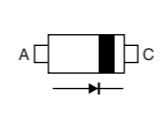
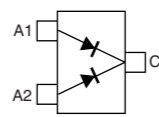
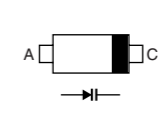
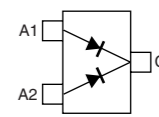
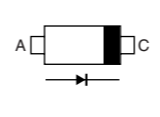
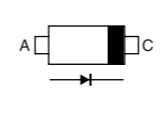
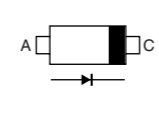
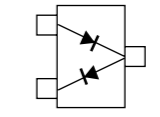
**UNIT abbreviations** (C): CHASSIS PARTS, (M): MAIN UNIT, (P): PA UNIT, (CN): CONNECT UNIT, (A): ANT UNIT

# SECTION 9 SEMICONDUCTOR INFORMATION

## • TRANSISTORS AND FET's

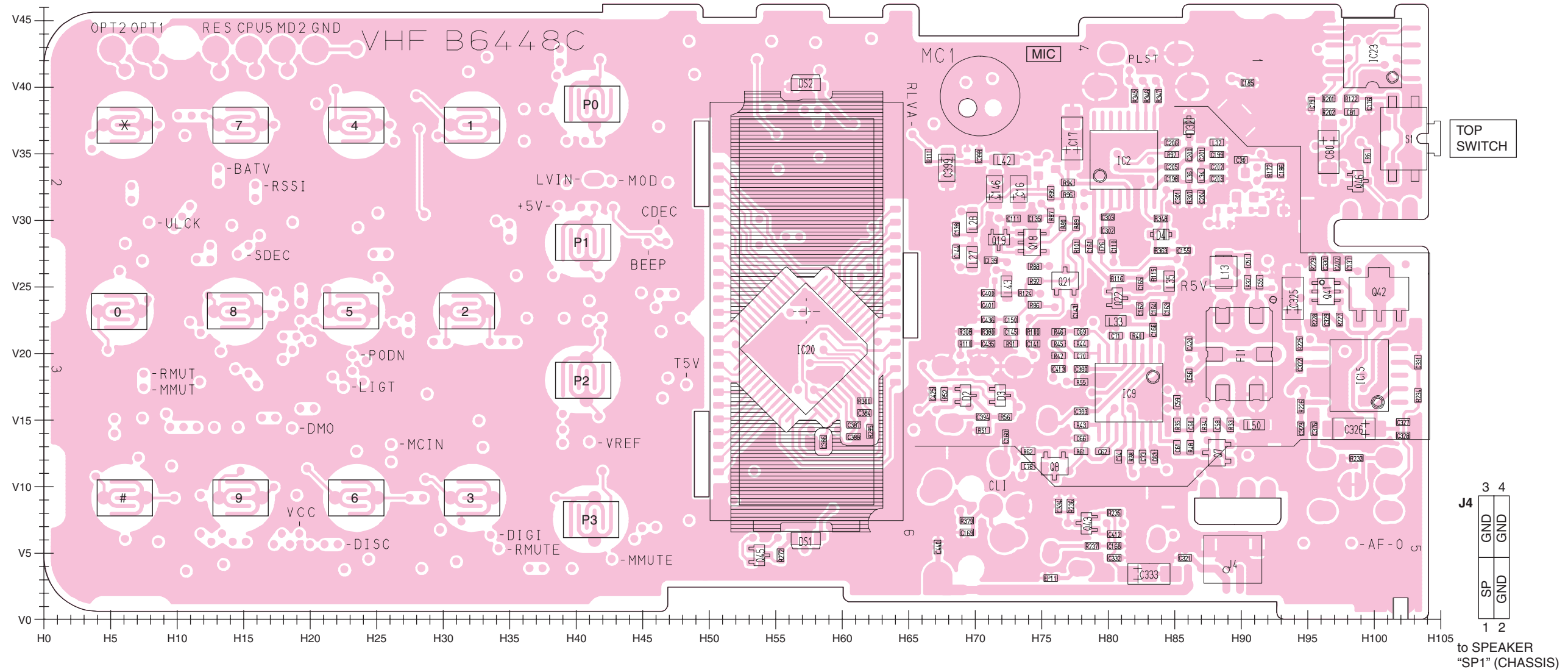
<b>2SA1577 T106 R</b> (Symbol: HR) 	<b>2SB1132 T100 R</b> (Symbol: BAR) 	<b>2SC4081 T106 R</b> (Symbol: BR) 	<b>2SC4116 BL</b> (Symbol: LL) 	<b>2SC5006 T1</b> (Symbol: 24) 
<b>2SC5107 O</b> (Symbol: MFO) 	<b>2SC5700</b> (Symbol: WB-) 	<b>2SK3019</b> (Symbol: KN) 	<b>2SK880 Y</b> (Symbol: XY) 	<b>3SK293</b> (Symbol: UF) 
<b>3SK324UG-TL-E</b> (Symbol: UG-) 	<b>RD01MUS1</b> (Symbol: K2) 	<b>RD07MVS1</b> (Symbol: RD07MVS1) 	<b>UNR9111J</b> (Symbol: 6A) 	<b>UNR9113J</b> (Symbol: 6C) 
<b>UNR9210J</b> (Symbol: 8L) 	<b>UNR9213J</b> (Symbol: 8C) 	<b>XP1214</b> (Symbol: 9H) 	<b>XP4601</b> (Symbol: 5C) 	<b>XP6401</b> (Symbol: 5O) 
<b>XP6501 AB</b> (Symbol: 5N) 				

## • DIODES

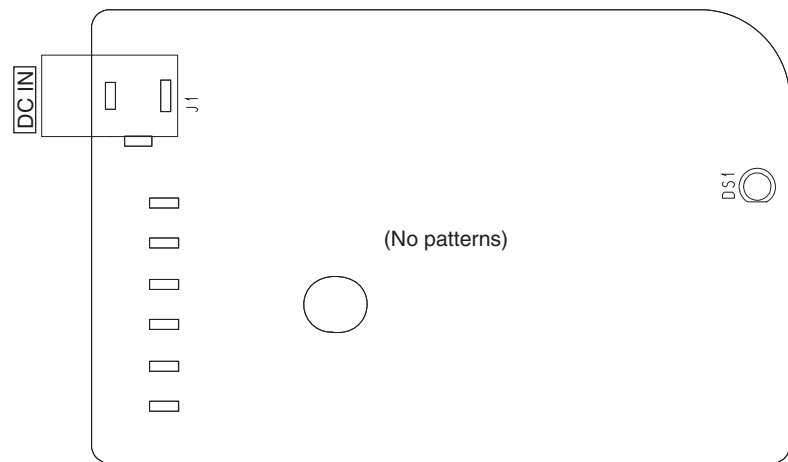
<b>1SV239</b> (Symbol: TC) 	<b>1SV307</b> (Symbol: TX) 	<b>DAN222TL</b> (Symbol: N) 	<b>HVC375B</b> (Symbol: B8) 	<b>HVC376B</b> (Symbol: B9) 
<b>MA2S077</b> (Symbol: S) 	<b>MA2S111</b> (Symbol: A) 	<b>MA2S728</b> (Symbol: B) 	<b>RB706F-40 T106</b> (Symbol: 3J) 	

# SECTION 10 BOARD LAYOUTS

## • MAIN UNIT (TOP VIEW)

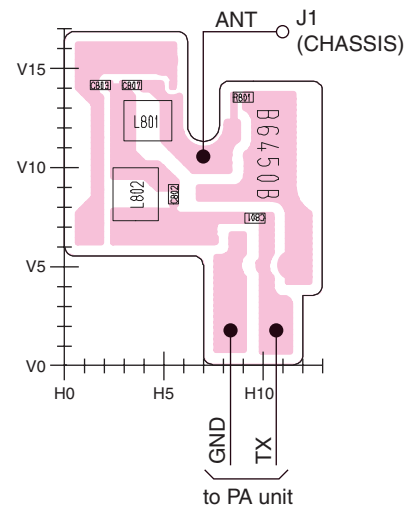


## • BC-160 (TOP VIEW)

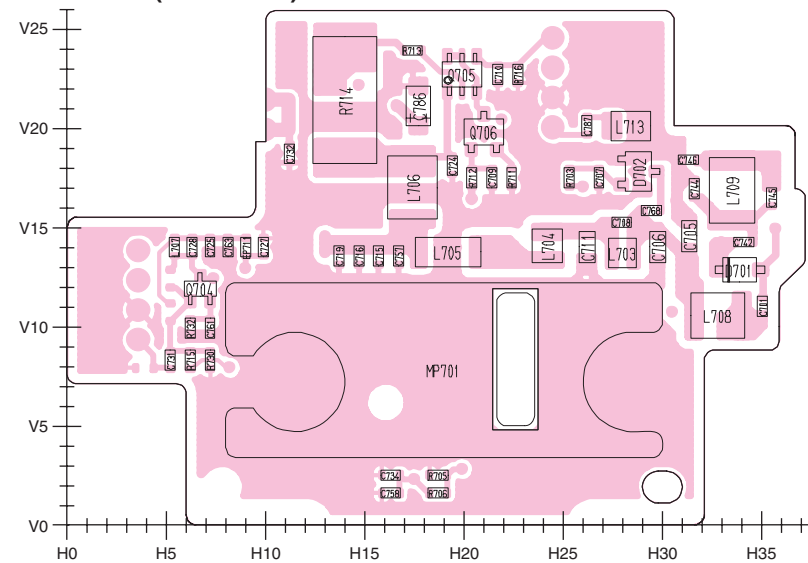


10 - 1

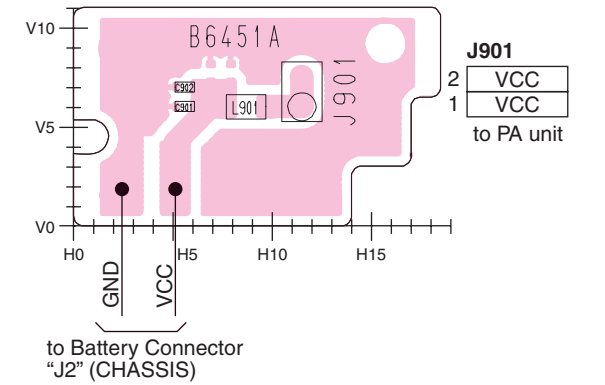
## • ANT UNIT (TOP VIEW)



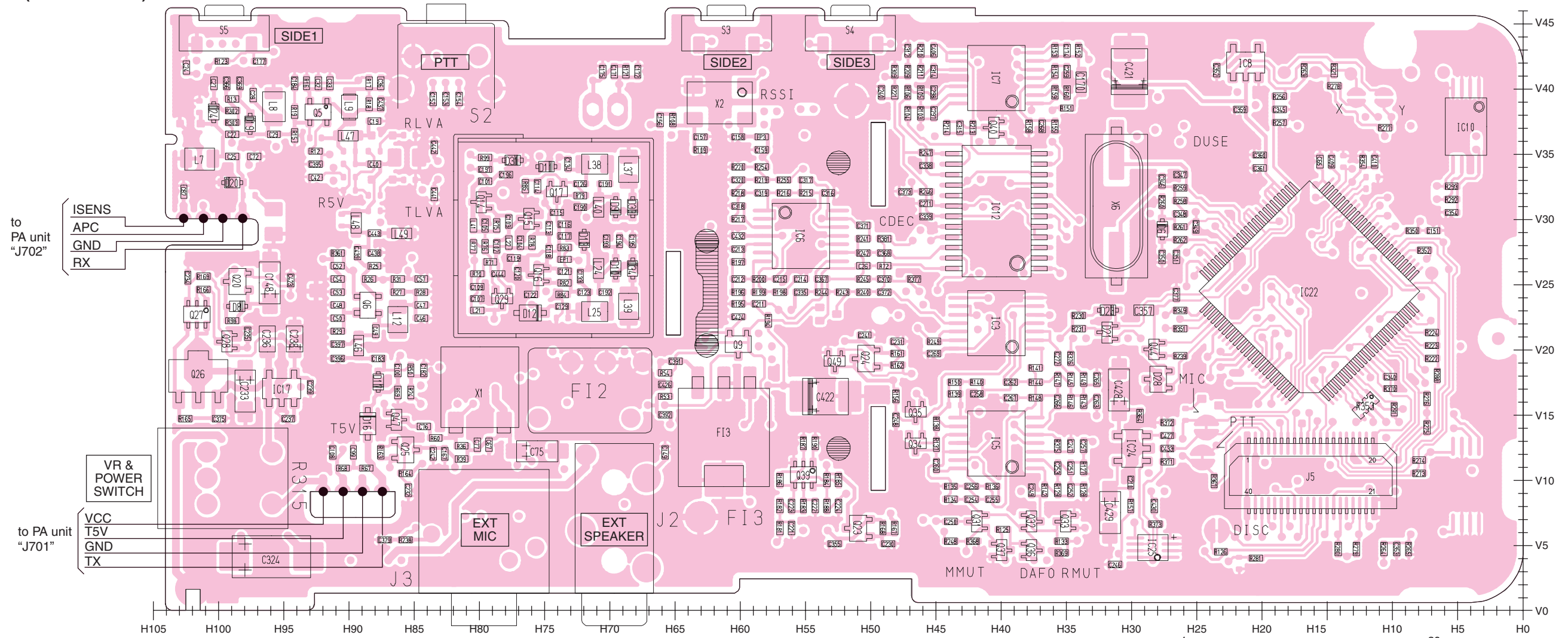
## • PA UNIT (TOP VIEW)



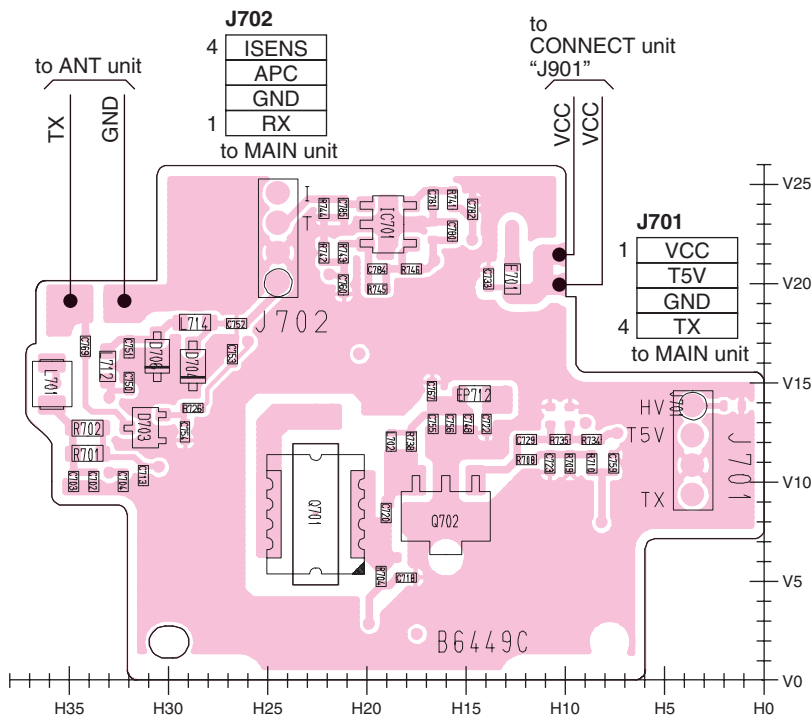
## • CONNECT UNIT (TOP VIEW)



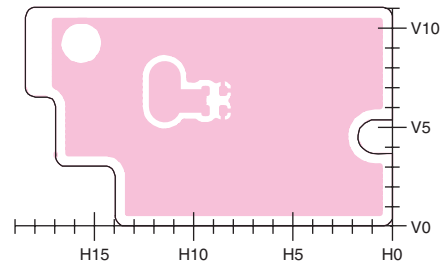
• MAIN UNIT (BOTTOM VIEW)



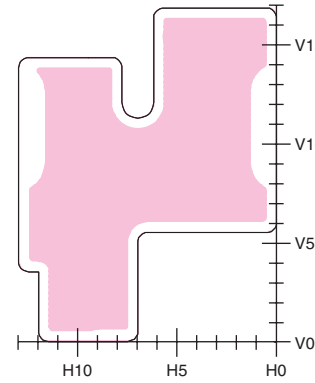
• PA UNIT (BOTTOM VIEW)



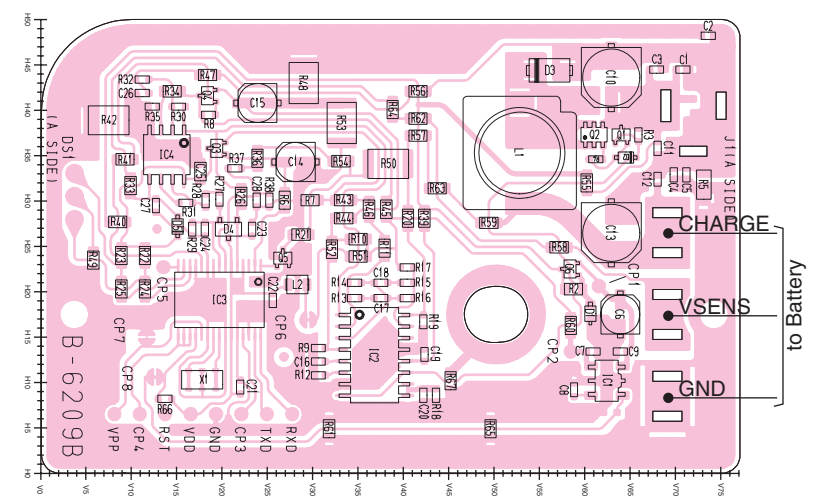
• CONNECT UNIT (BOTTOM VIEW)



• ANT UNIT (BOTTOM VIEW)

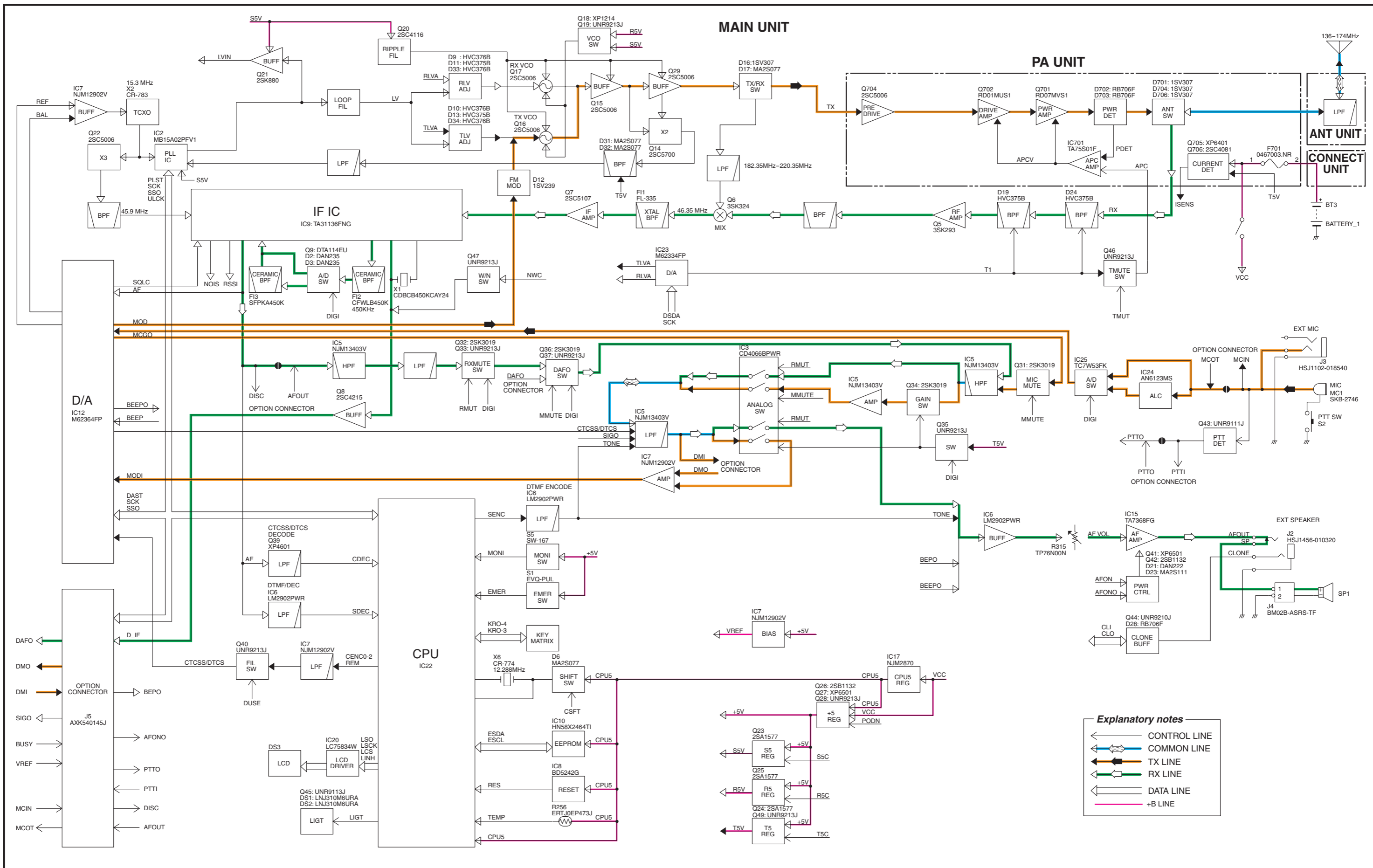


• BC-160 (BOTTOM VIEW)

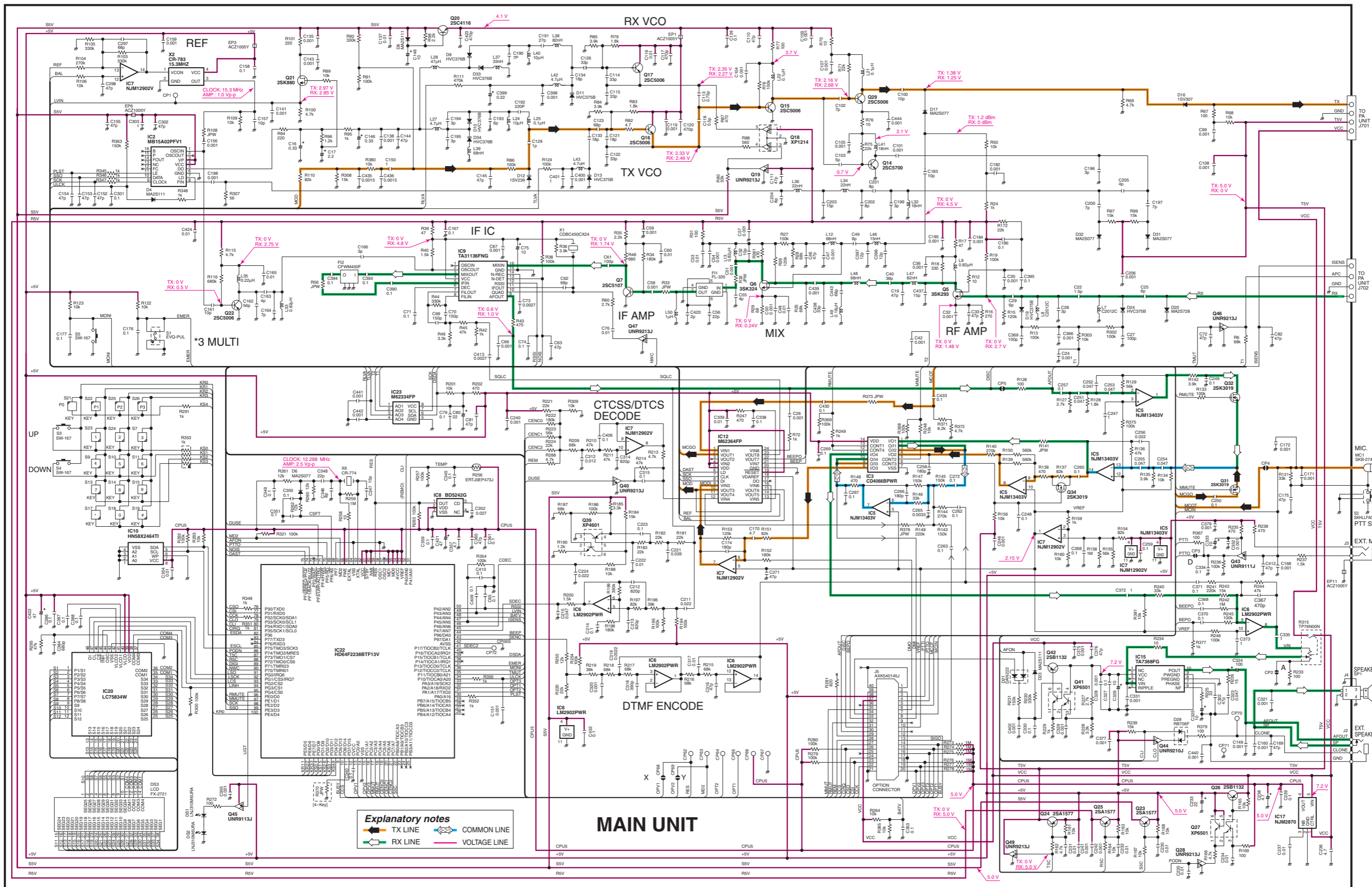


1	D	IF	GND	P1T1	MCOT	DMO	MCIN	BEFO	DMI	RMUT	NC	BREF	NC	AFOUT	DAFO	BUSY	SIGO	OPT1	OPT2	NC	OPT3	CSD	GND	OPV3	CCK	OPV2	GND	OPV1	20
2	D	IF	GND	+5V	VCC	MMUT	AFON	DAFO	REMS	CIRQ	NC	CSD	GND	OPV3	CCK	OPV2	GND	OPV1	20										
3	40																												
4																													21

# SECTION 11 BLOCK DIAGRAM



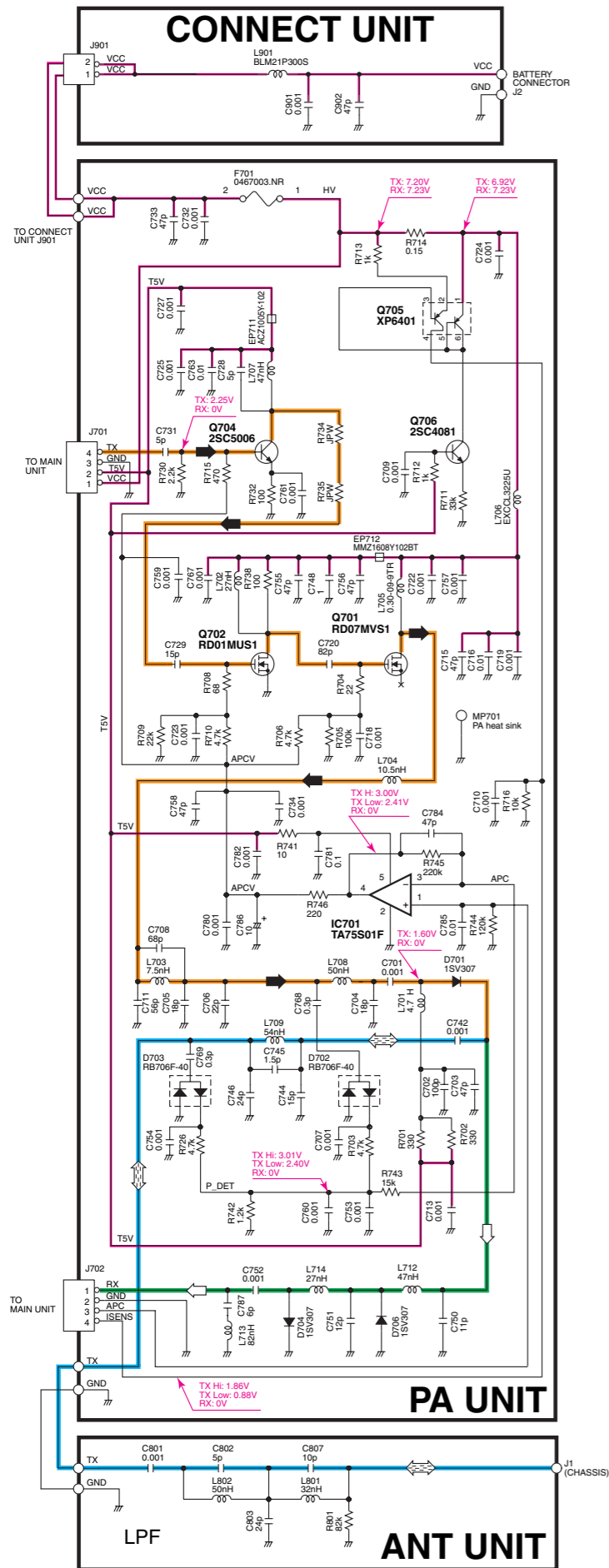
# SECTION 12 VOLTAGE DIAGRAMS



**Explanatory notes**  
 TX LINE (Orange arrow)  
 RX LINE (Green arrow)  
 COMMON LINE (Blue arrow)  
 VOLTAGE LINE (Pink arrow)

## MAIN UNIT





# SECTION 13 BC-160

## [CHASSIS PARTS]

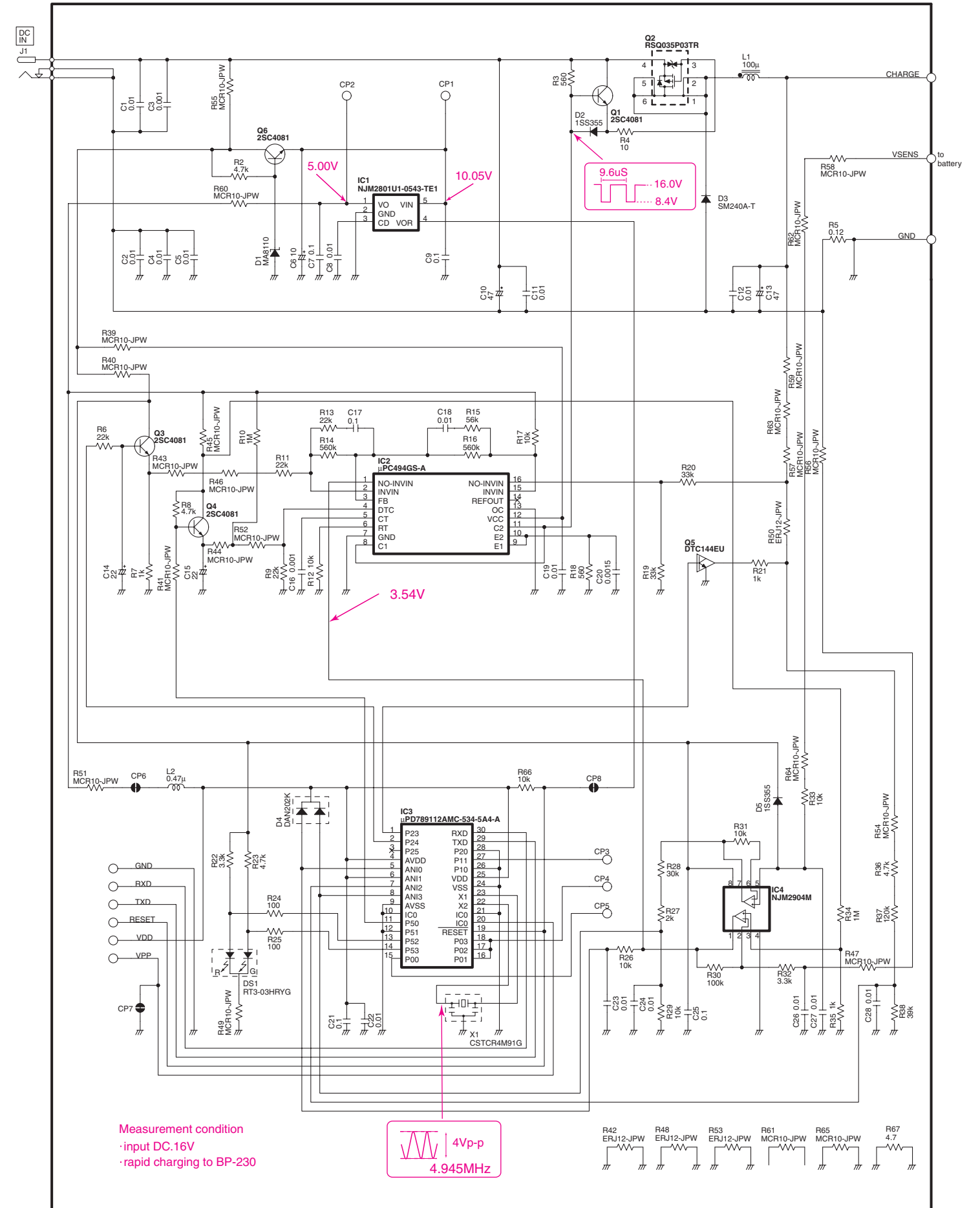
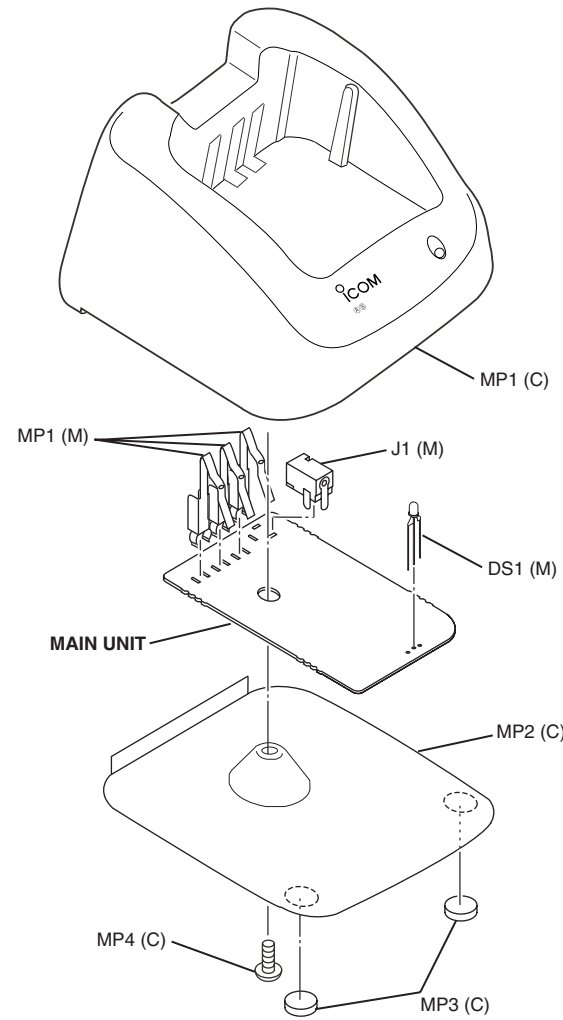
REF. NO.	ORDER NO.	DESCRIPTION	QTY.
MP1	8010019750	2830 case	1
MP2	8110008220	2830 cover	1
MP3	8930039620	Leg cushion (A)	2
MP4	8810008630	Screw PH BT M3 x 6 NI-ZU	1

## [MAIN UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
J1	6510023070	Connector HEC2305-01-250	1
DS1	5040002740	LED RT3-03HRYG	1
MP1	8930064410	2830 TERMINAL	3

## [ACCESSORIES]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
EP1	0800006050	Charger BC-145A	[USA] 1
	0800006060	Charger BC-145E	[EUR] 1



Measurement condition  
 ·input DC.16V  
 ·rapid charging to BP-230



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Fax : +81 (06) 6793 0013  
URL : <http://www.icom.co.jp/world/index.html>

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URL : <http://www.icomamerica.com>  
E-mail : [sales@icomamerica.com](mailto:sales@icomamerica.com)  
<Customer Service>  
Phone : +1 (425) 454-7619

### Icom Canada

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URL : <http://www.icomuk.co.uk>  
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